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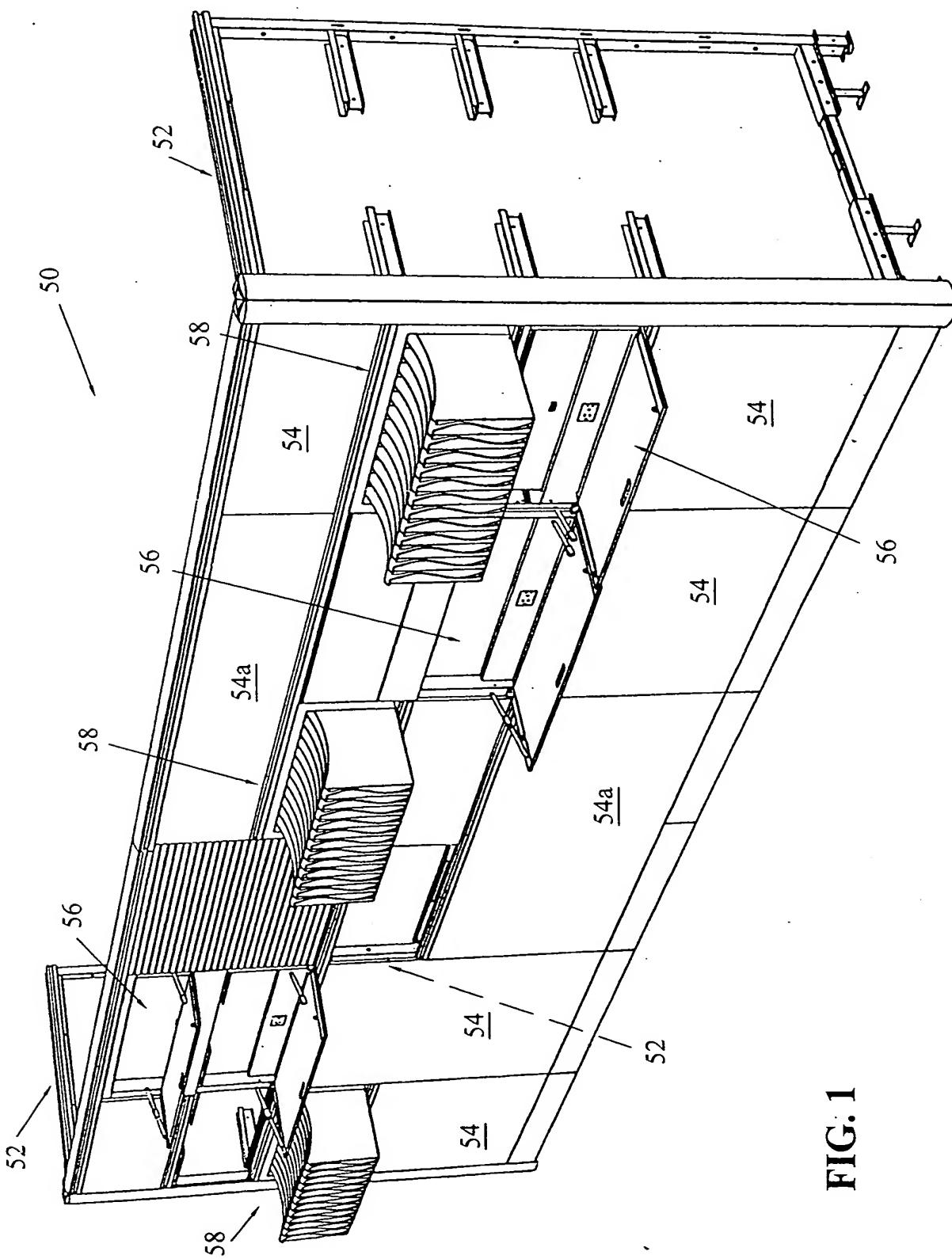


FIG. 1

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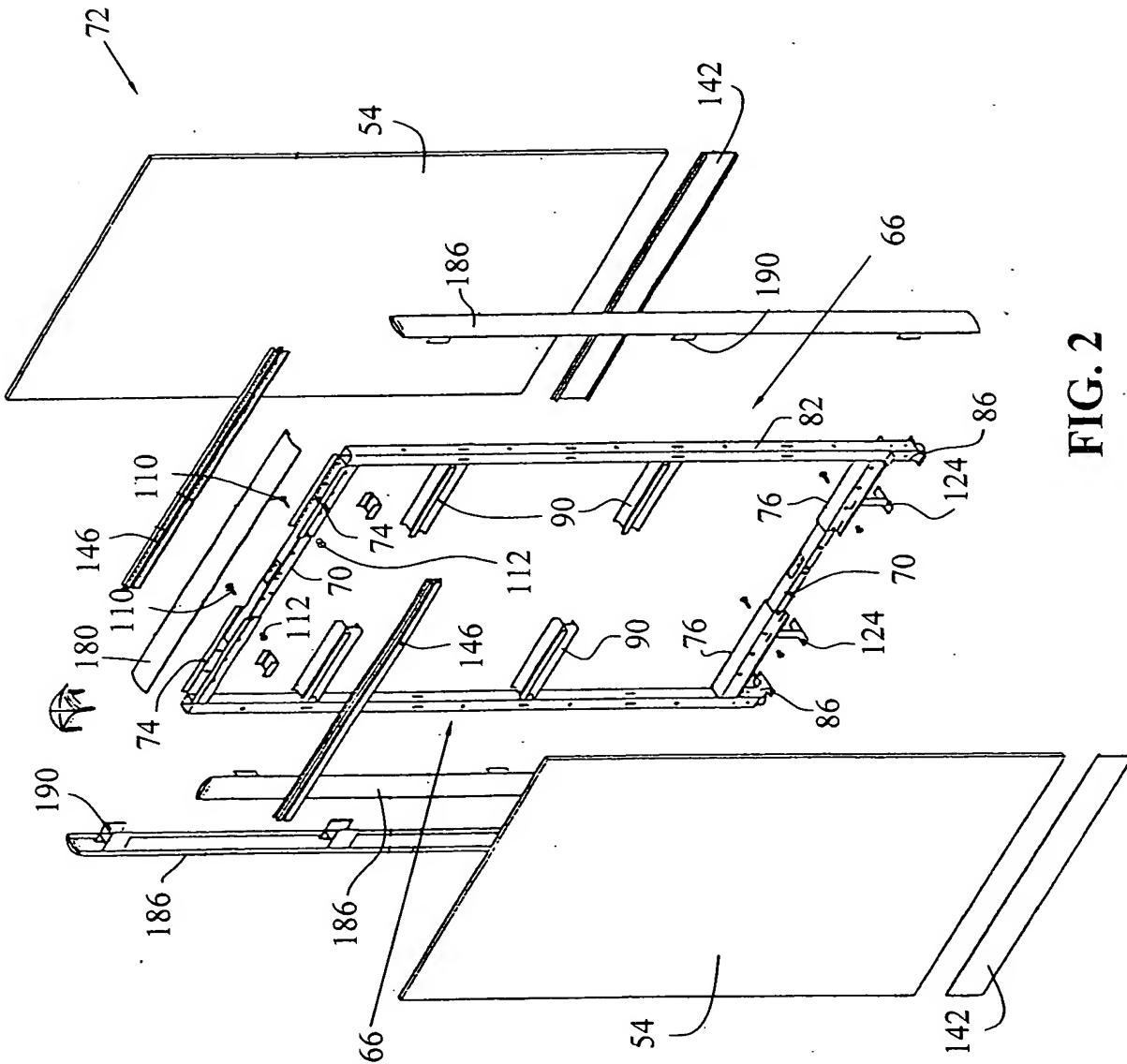


FIG. 2

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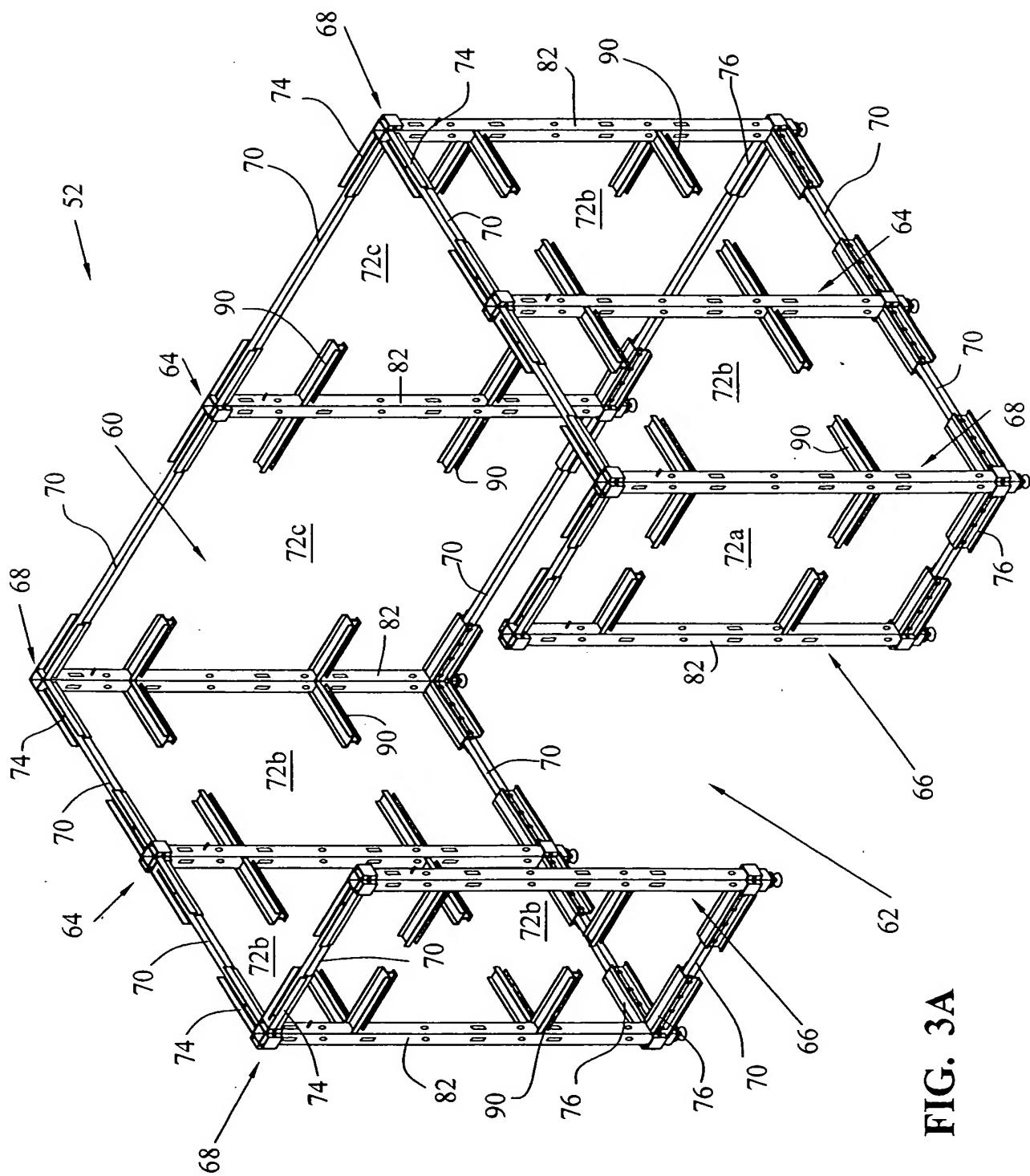


FIG. 3A

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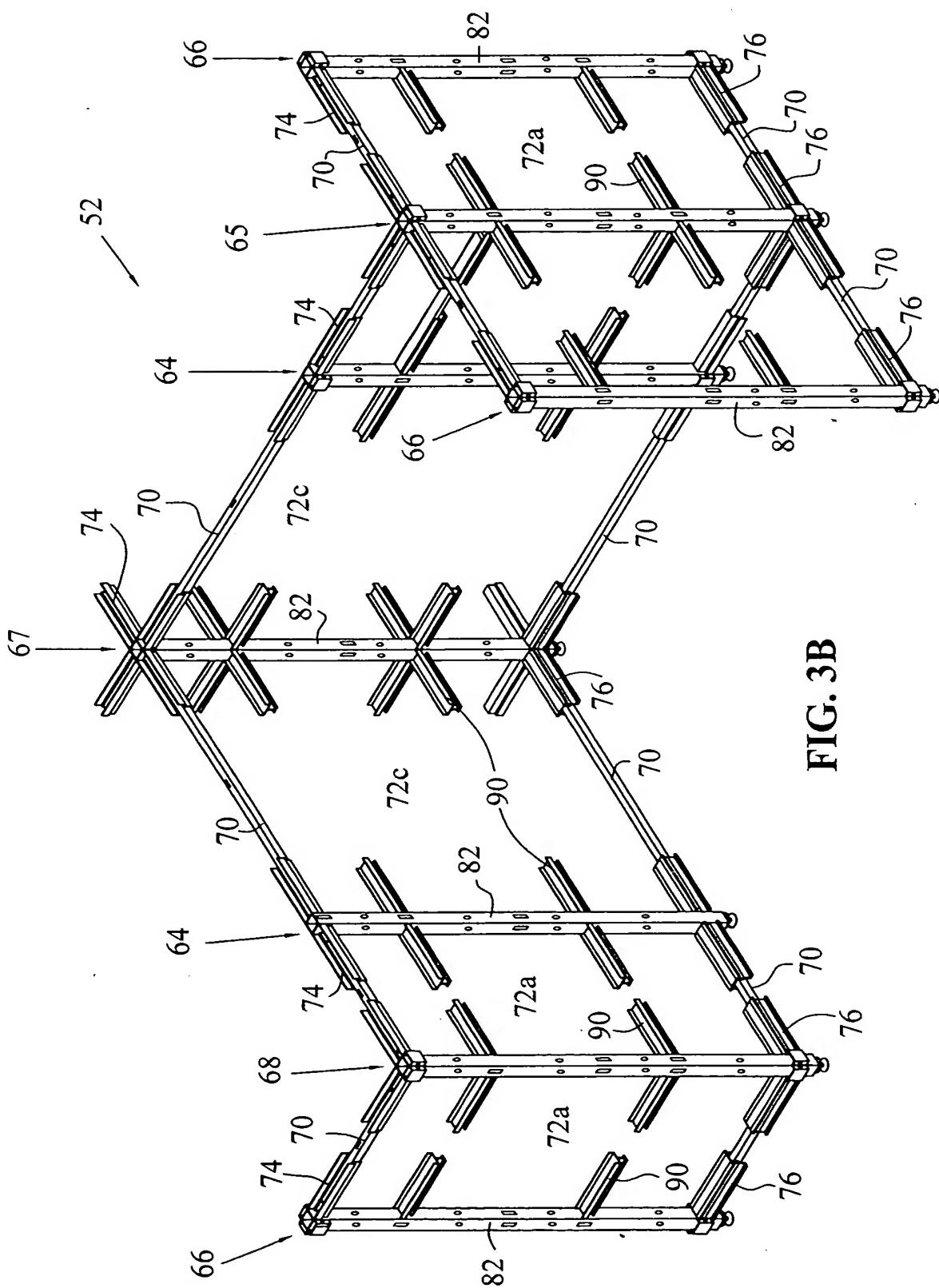


FIG. 3B

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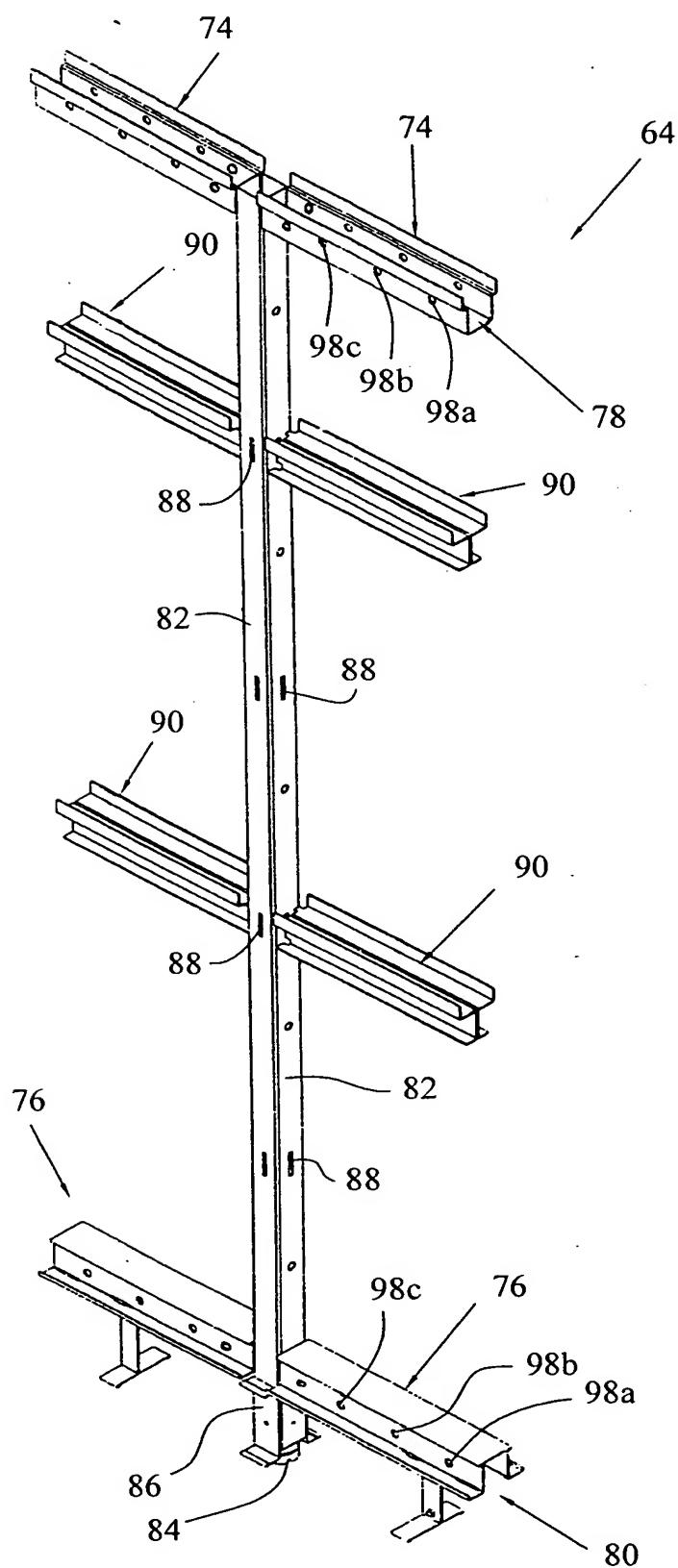


FIG. 4A

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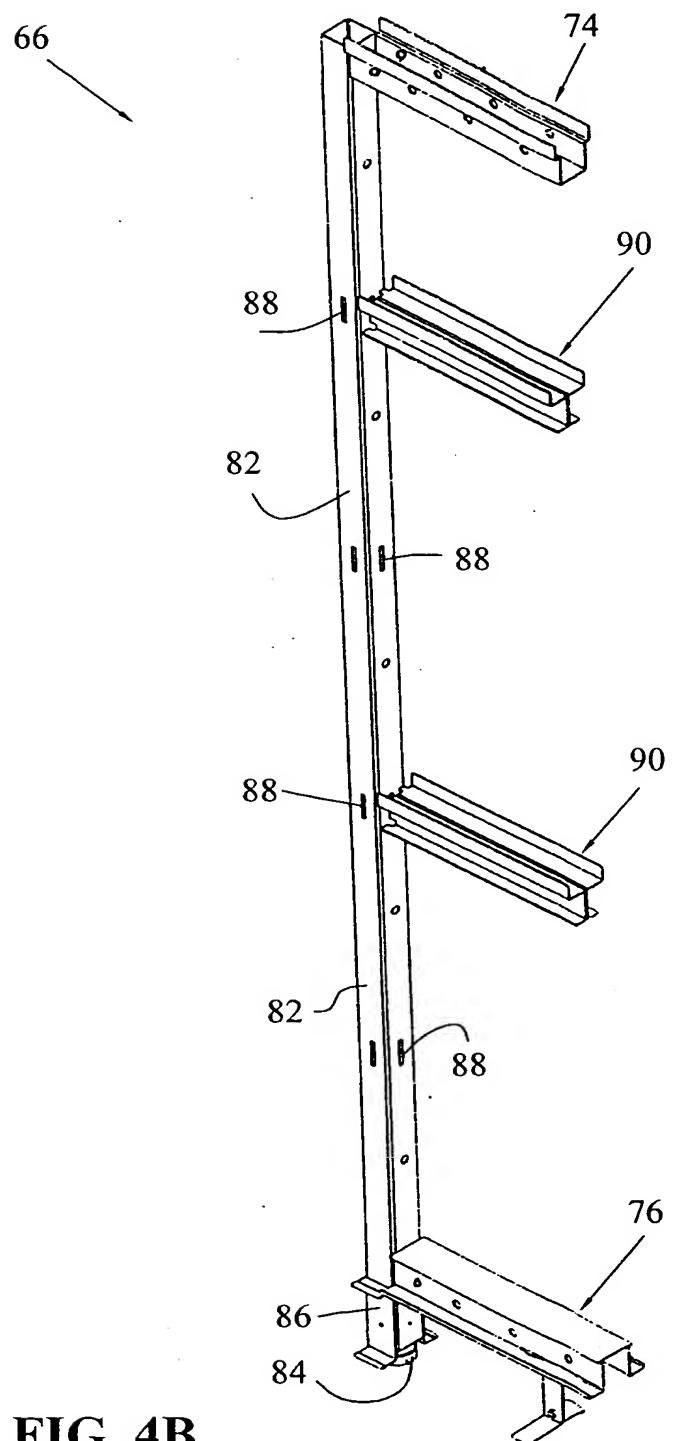


FIG. 4B

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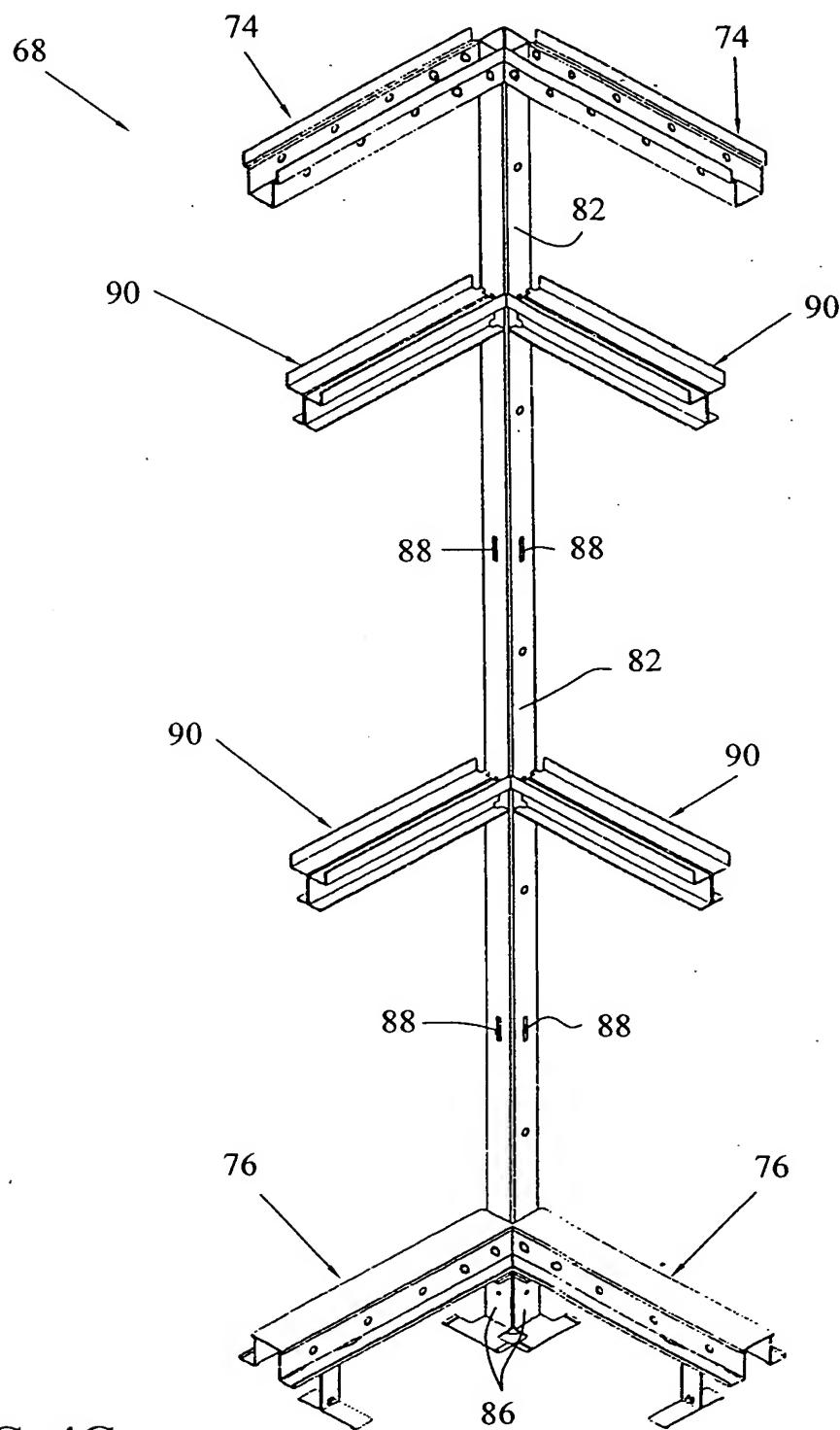
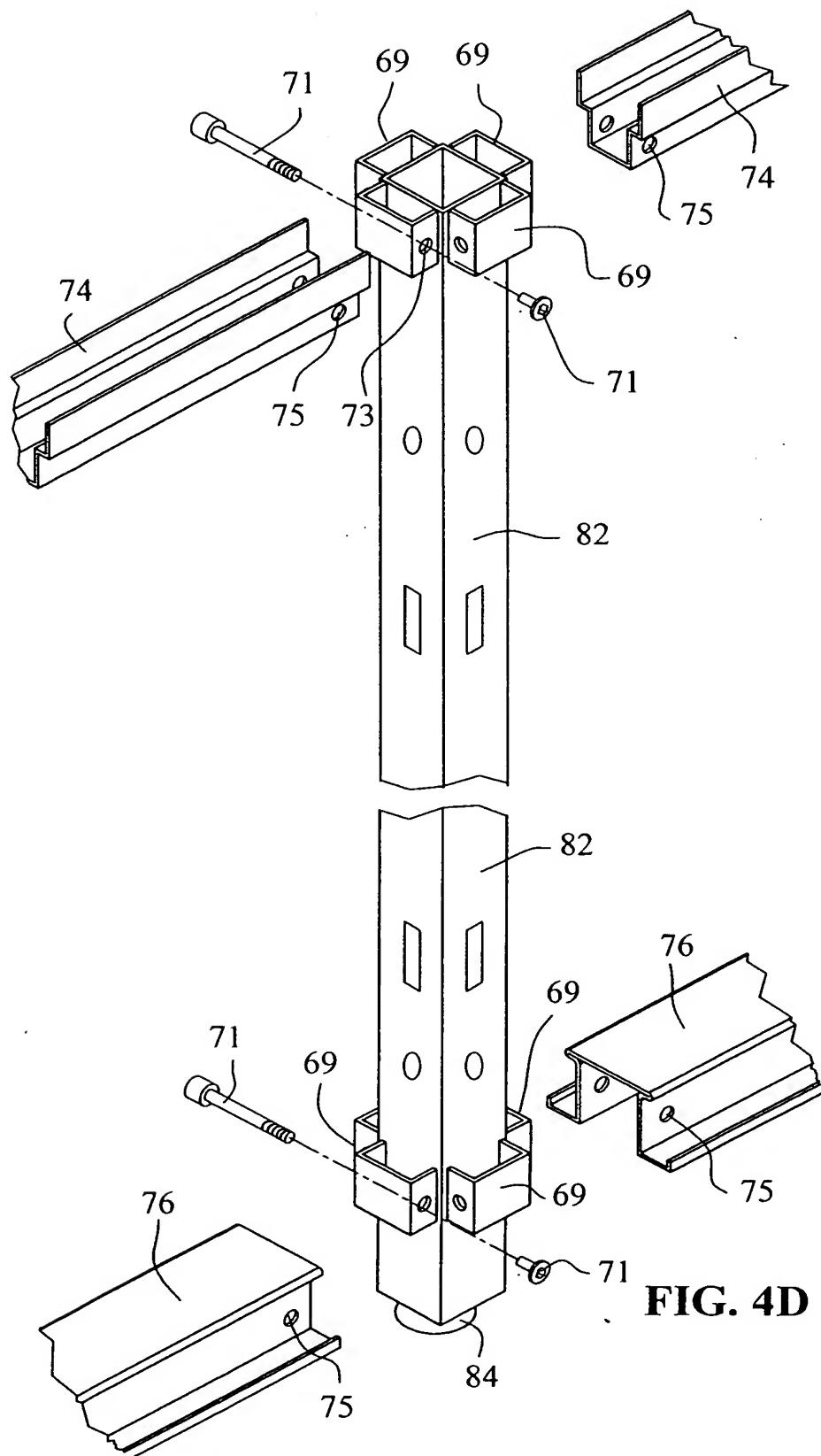


FIG. 4C

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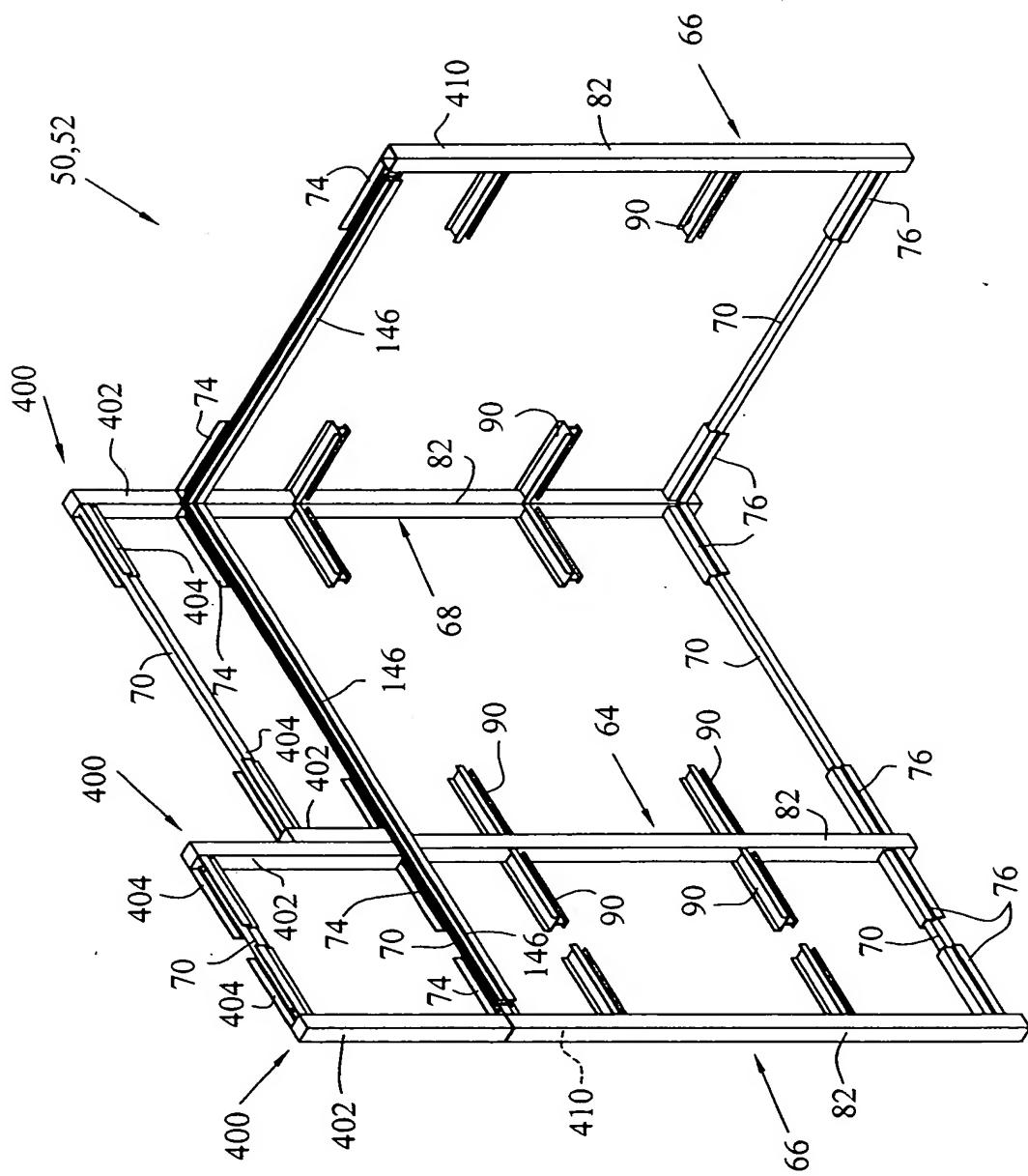


FIG. 5A

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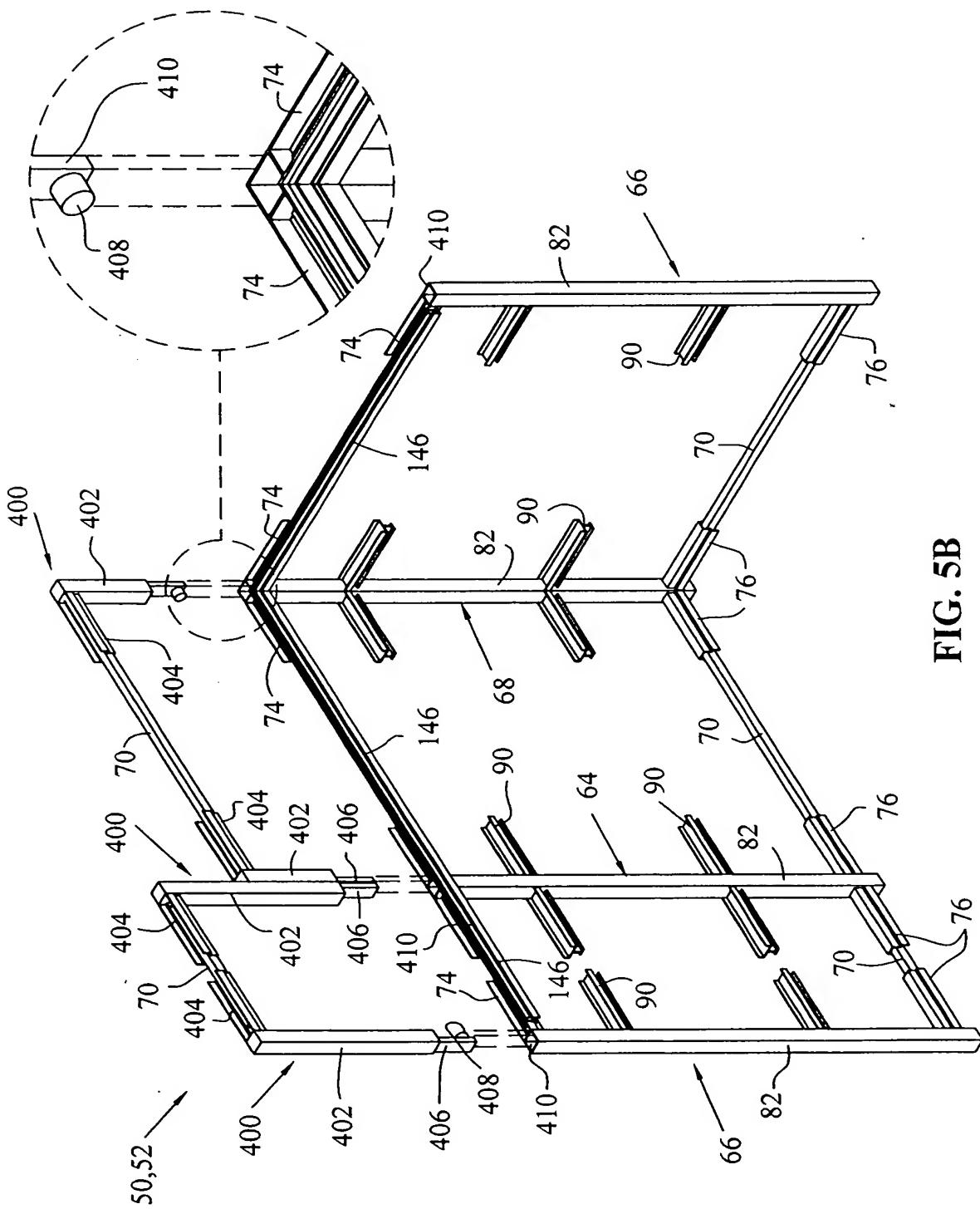


FIG. 5B

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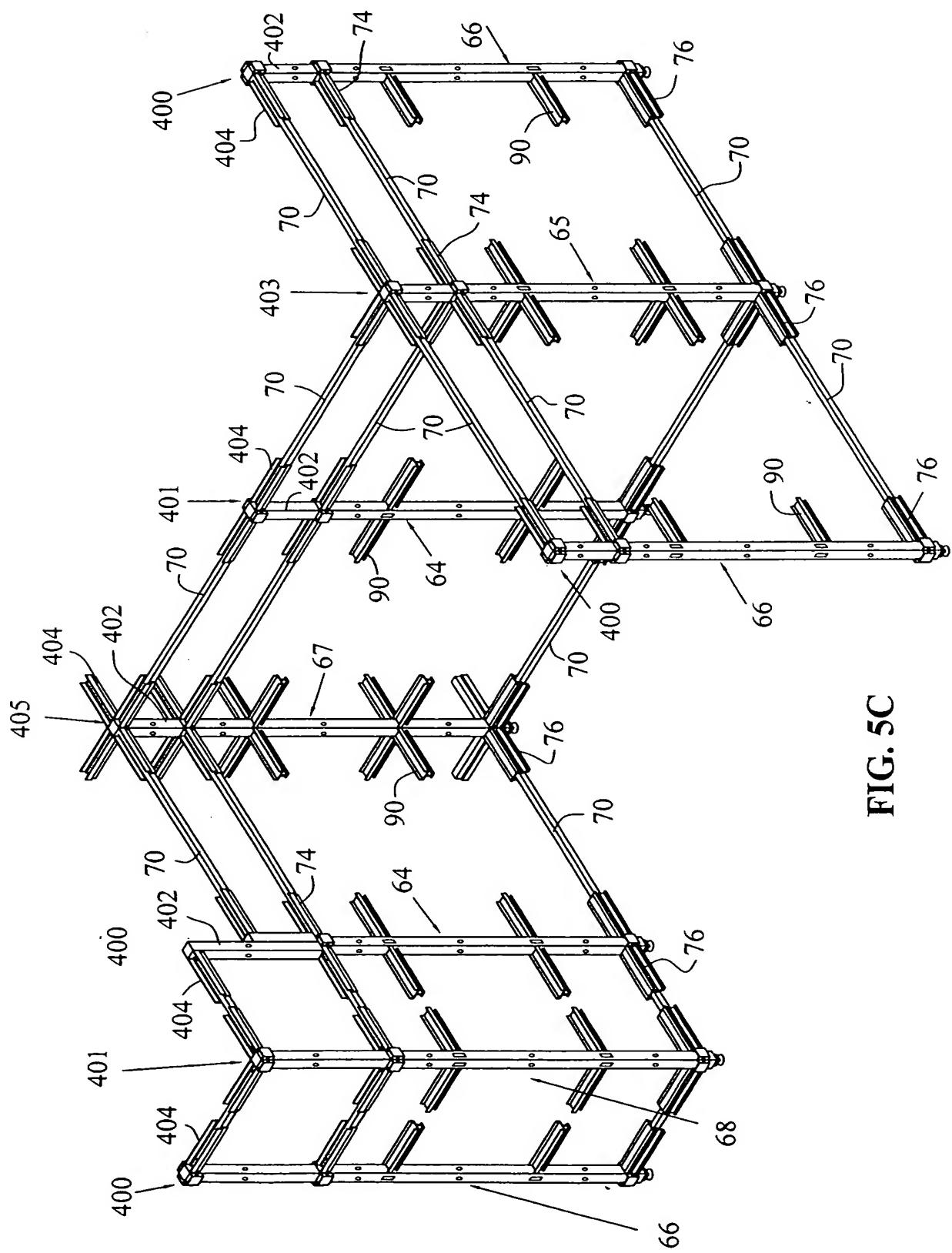


FIG. 5C

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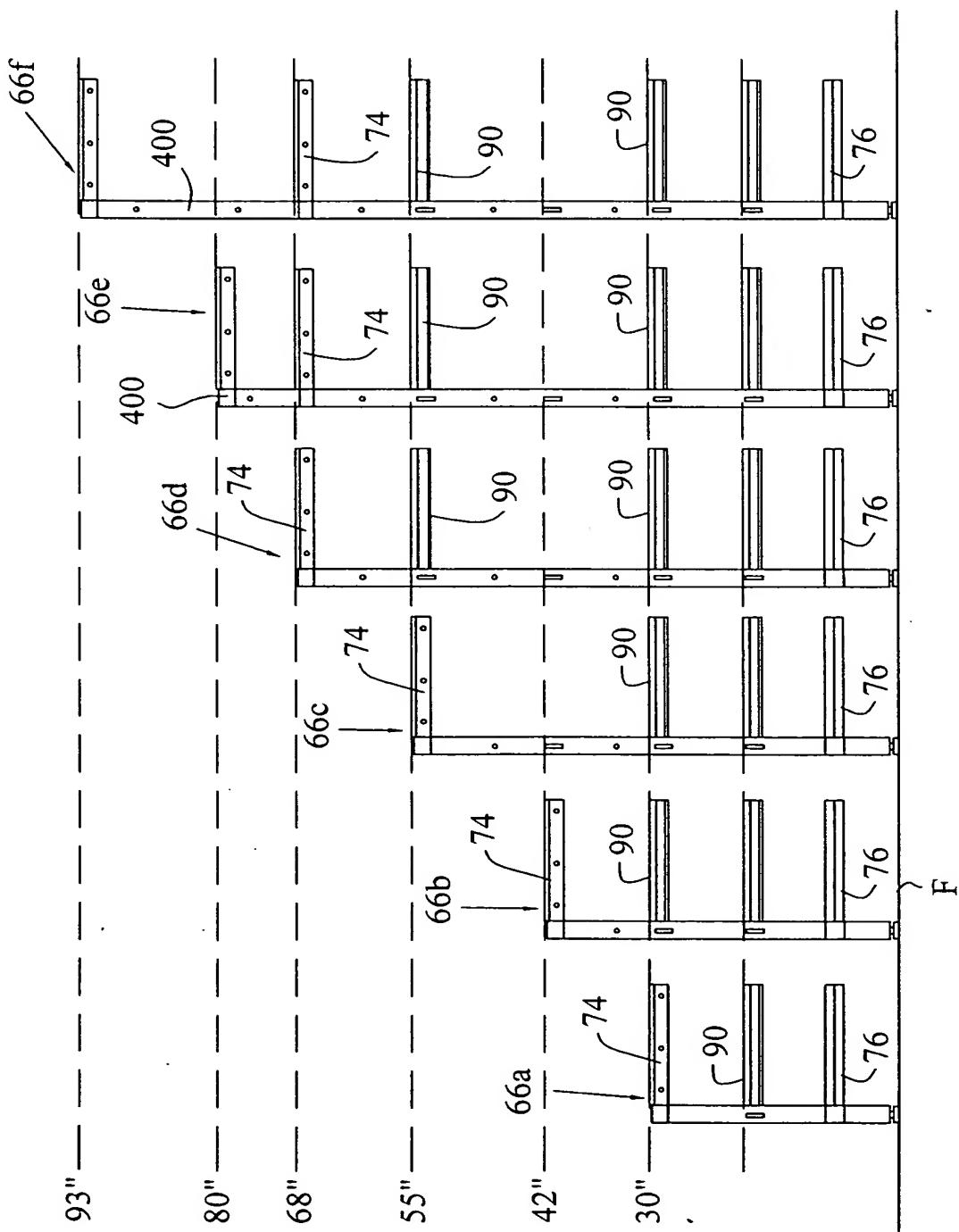
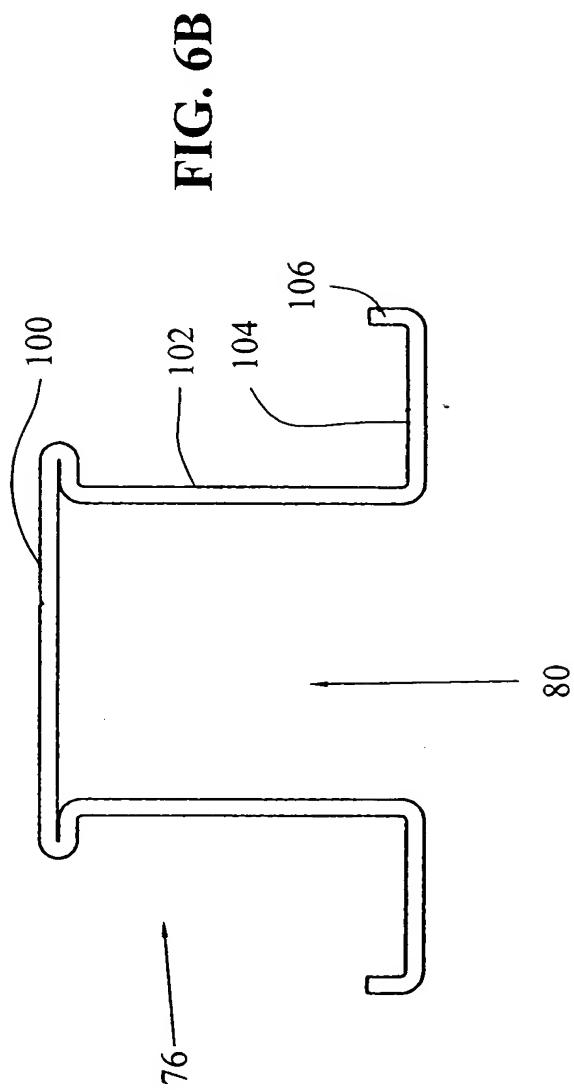
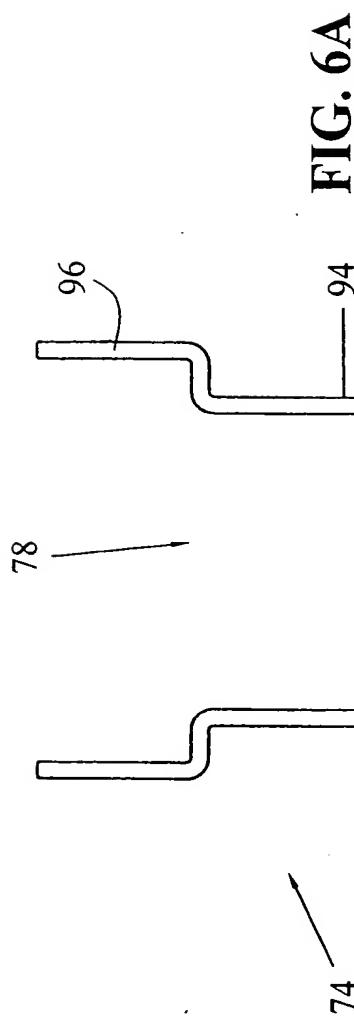


FIG. 5D

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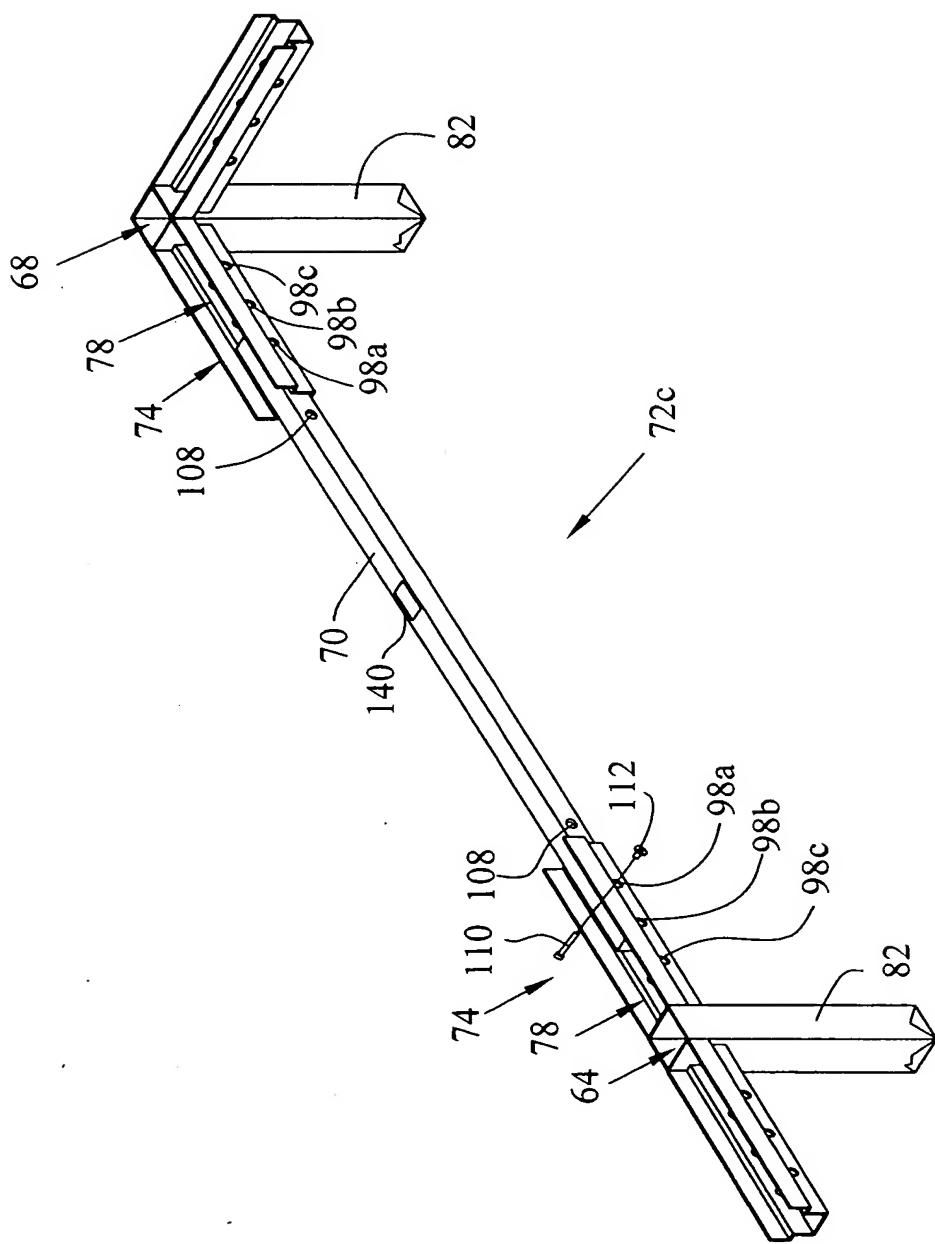


FIG. 7

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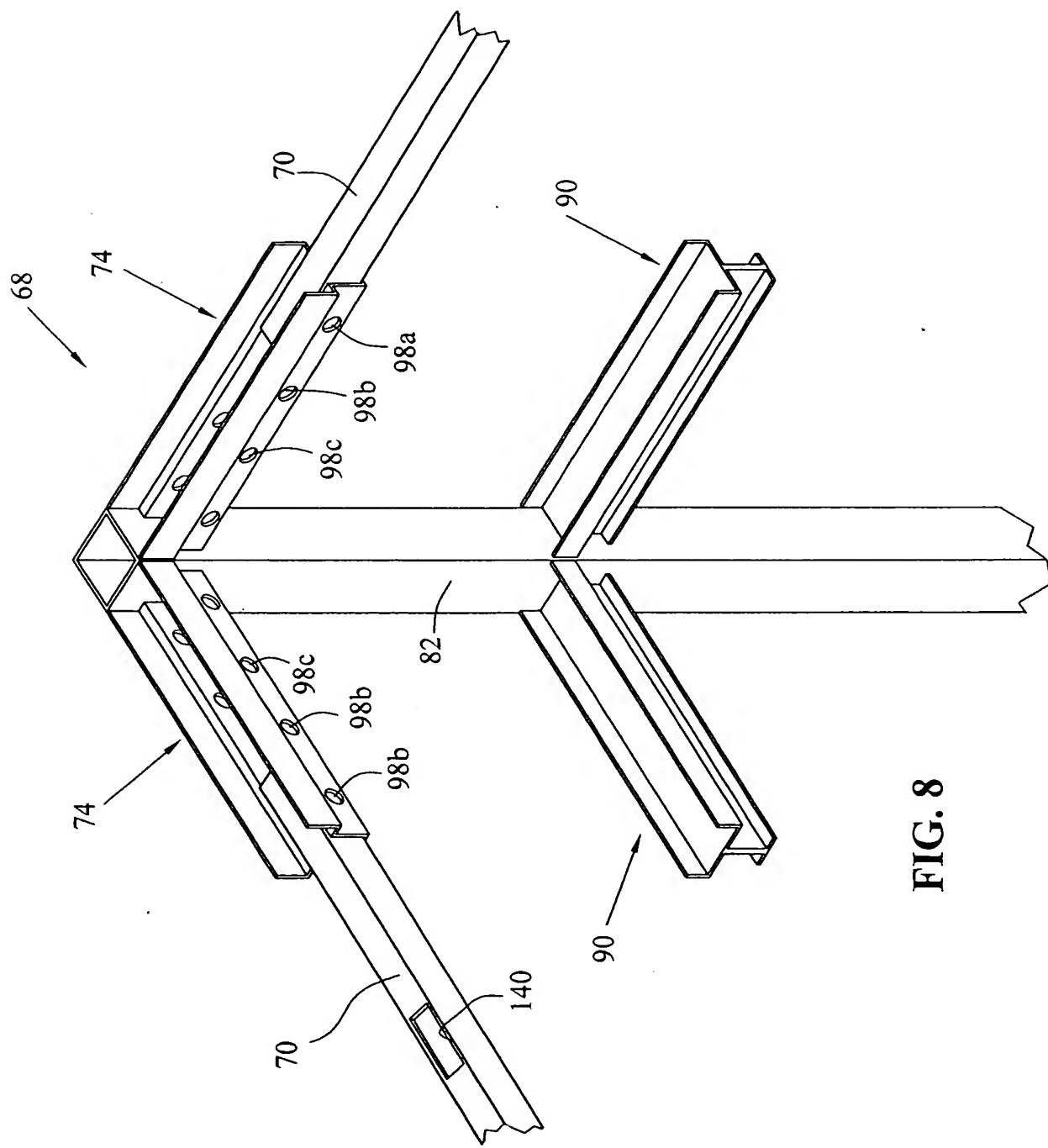


FIG. 8

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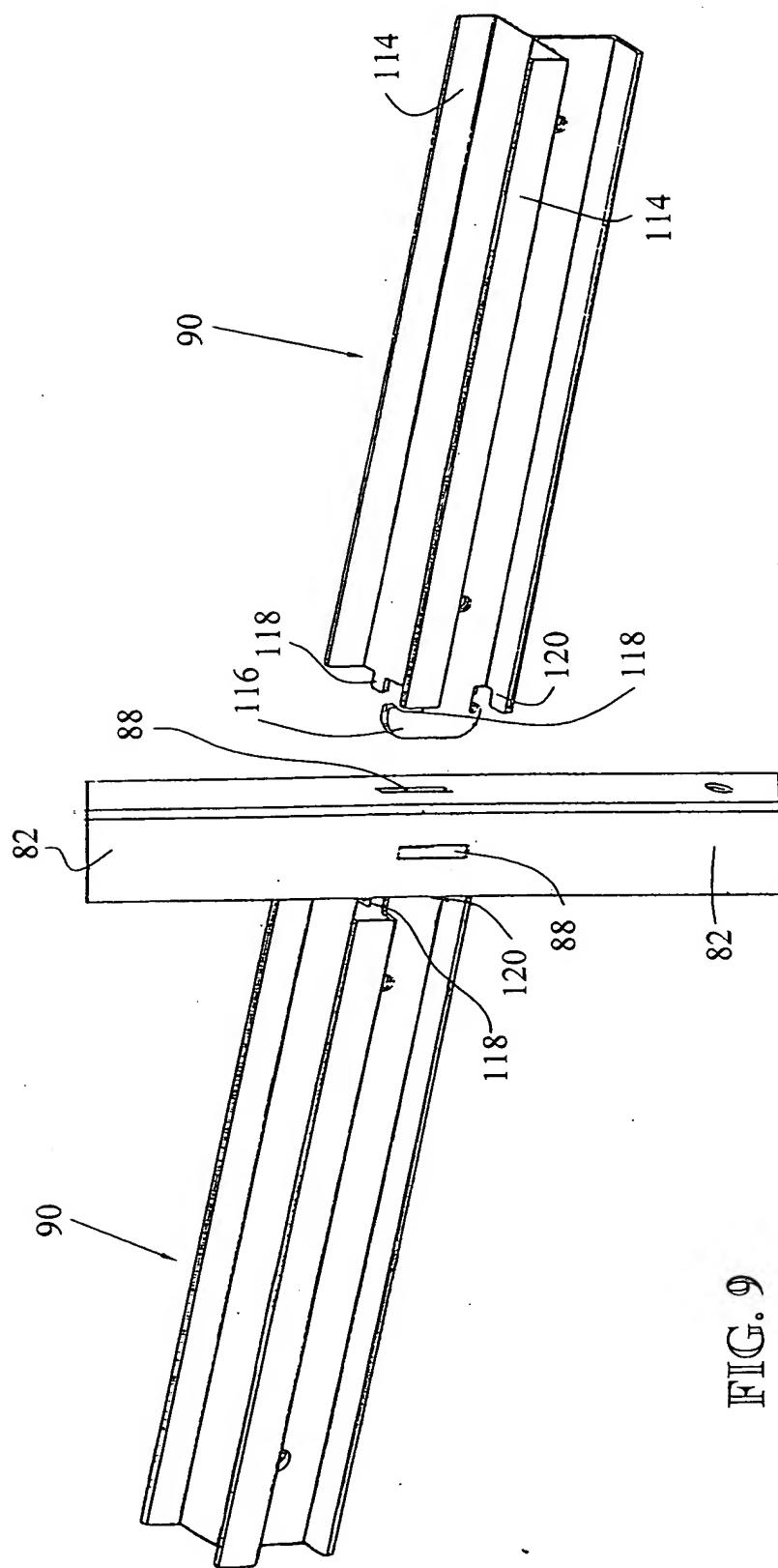


FIG. 9

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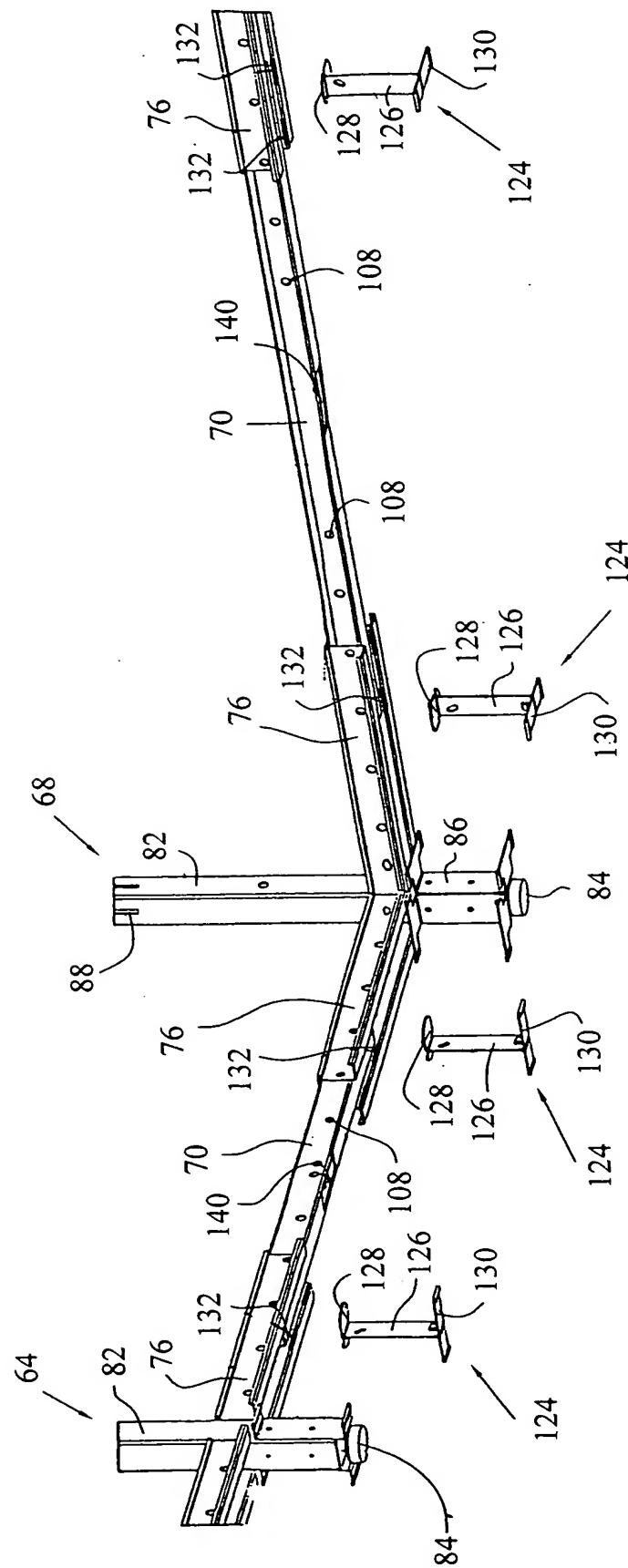


FIG. 10

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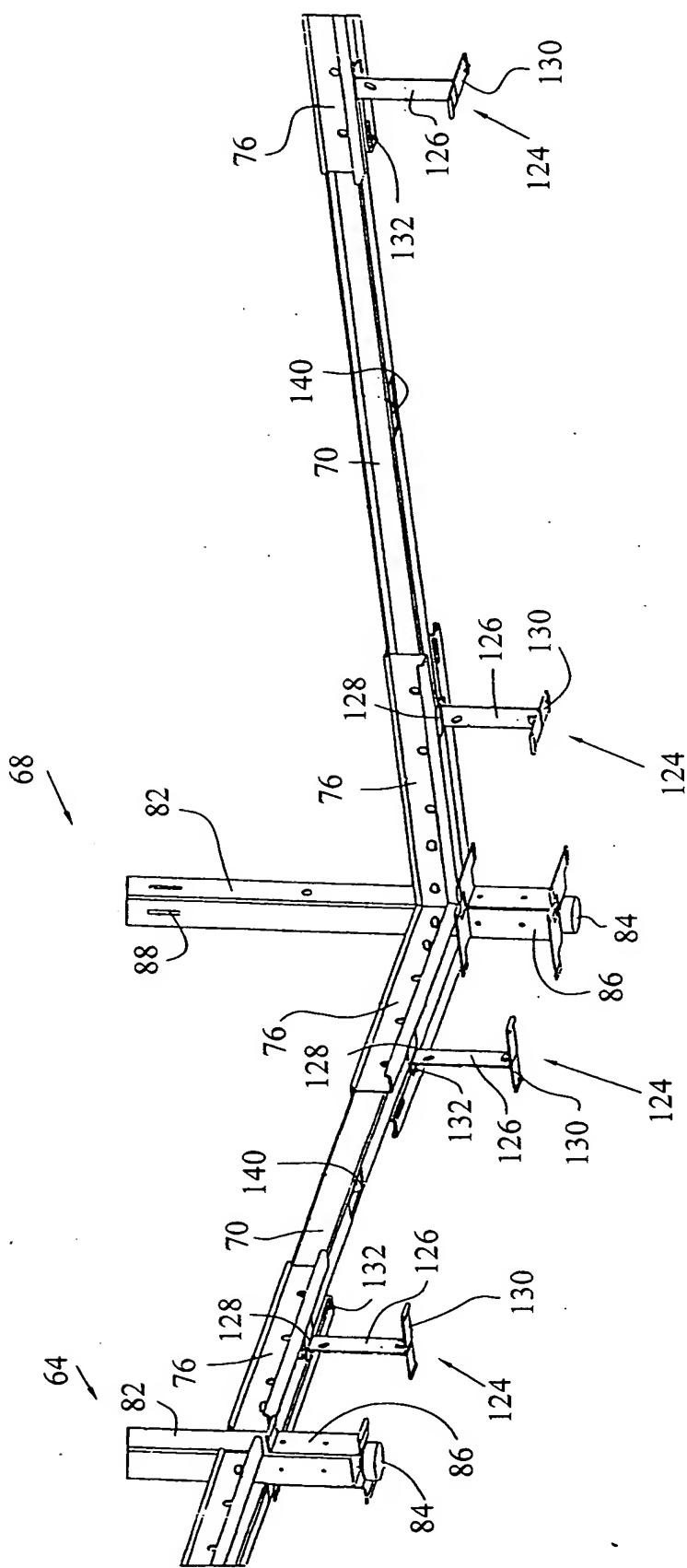


FIG. 11

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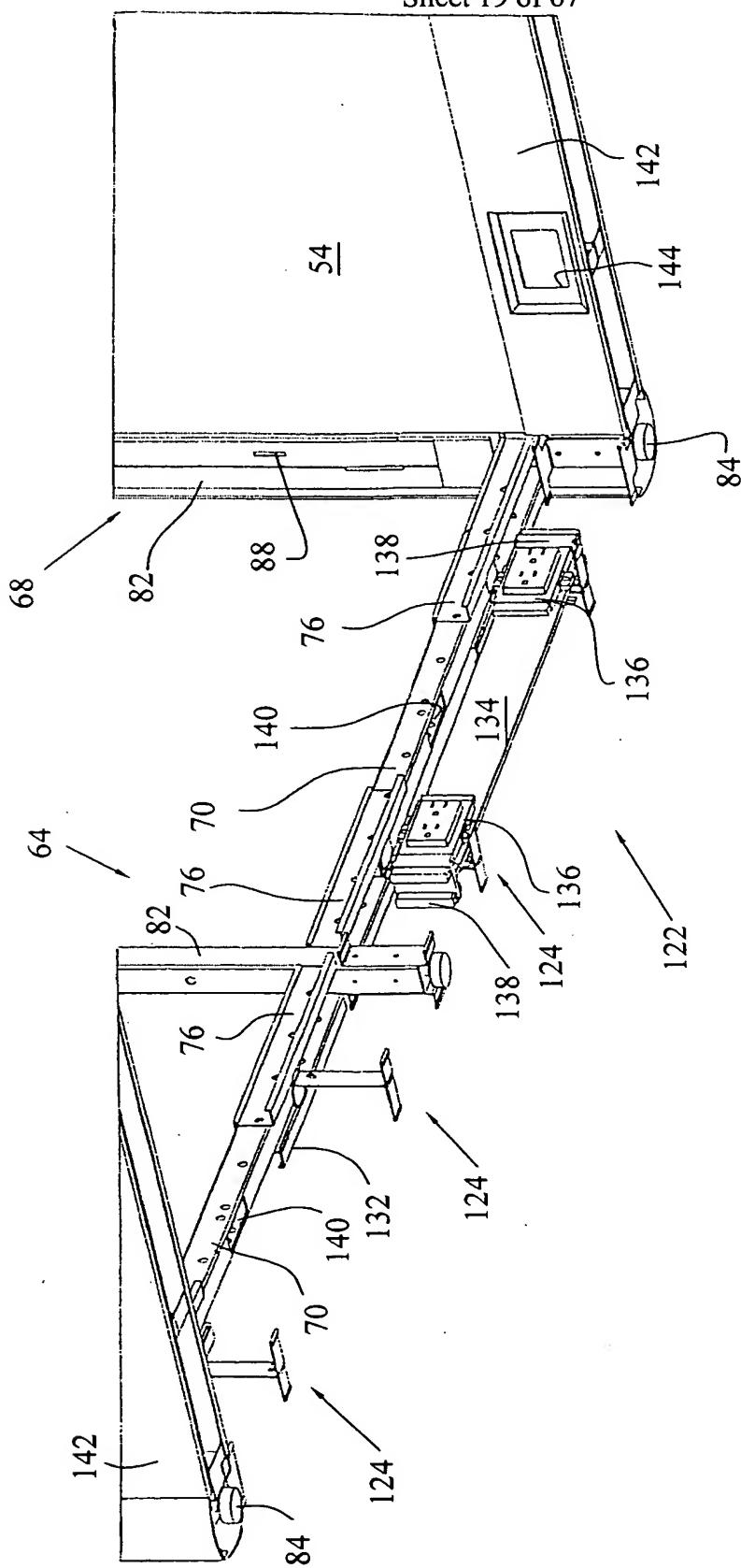


FIG. 12

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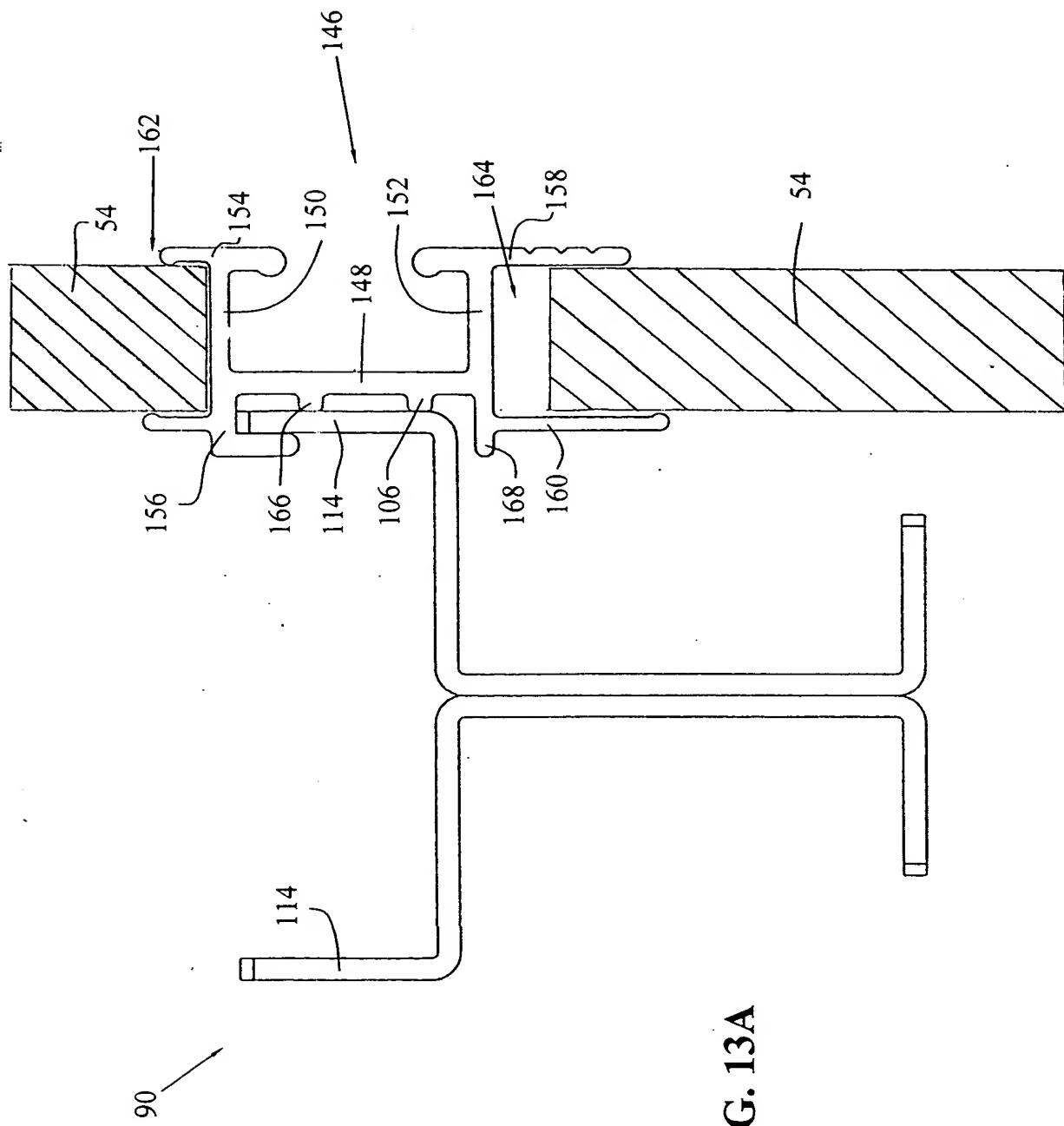


FIG. 13A

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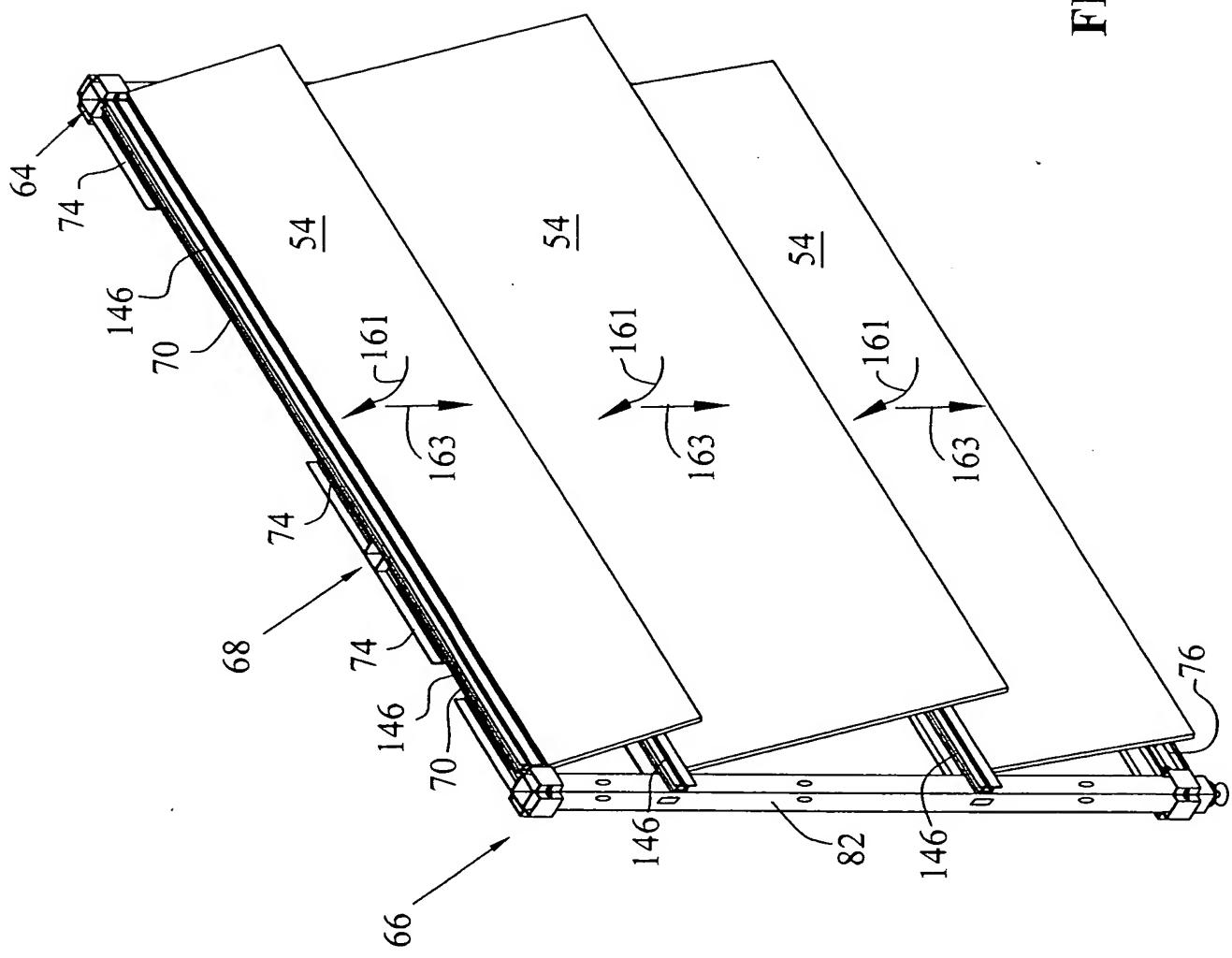


FIG. 13B

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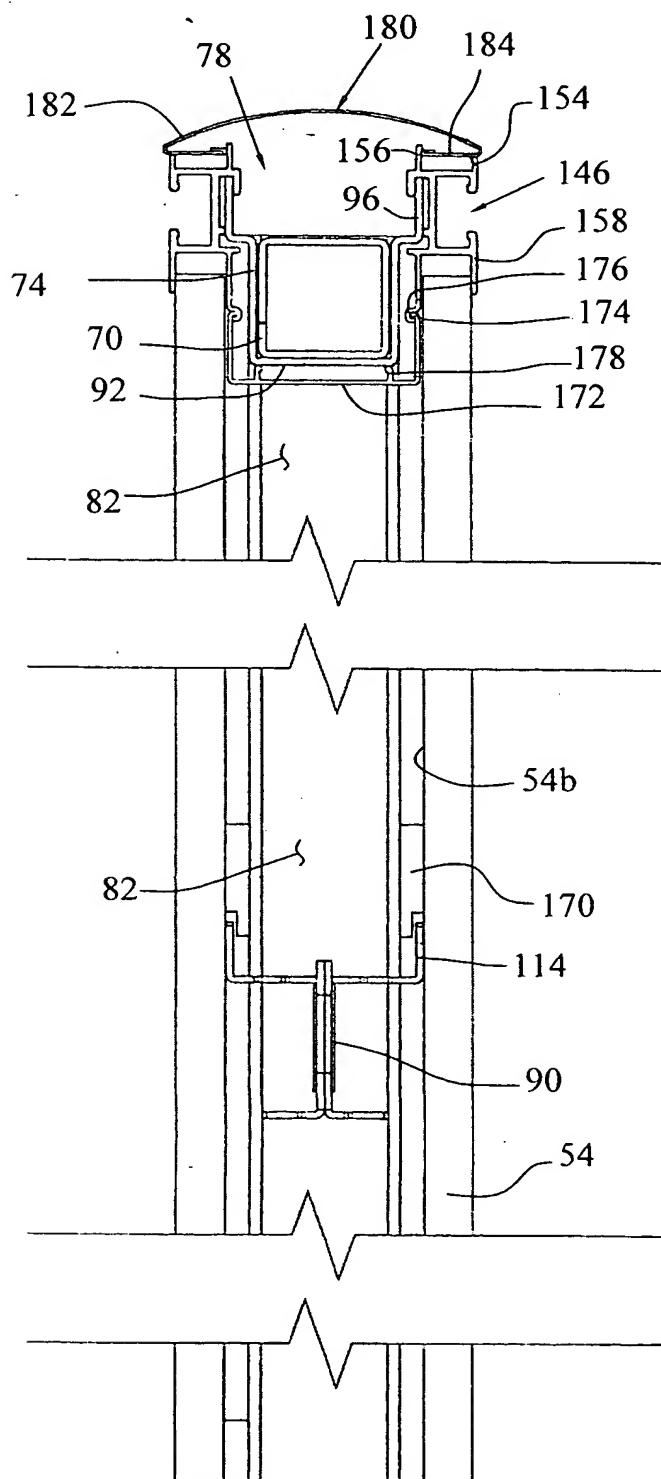


FIG. 14

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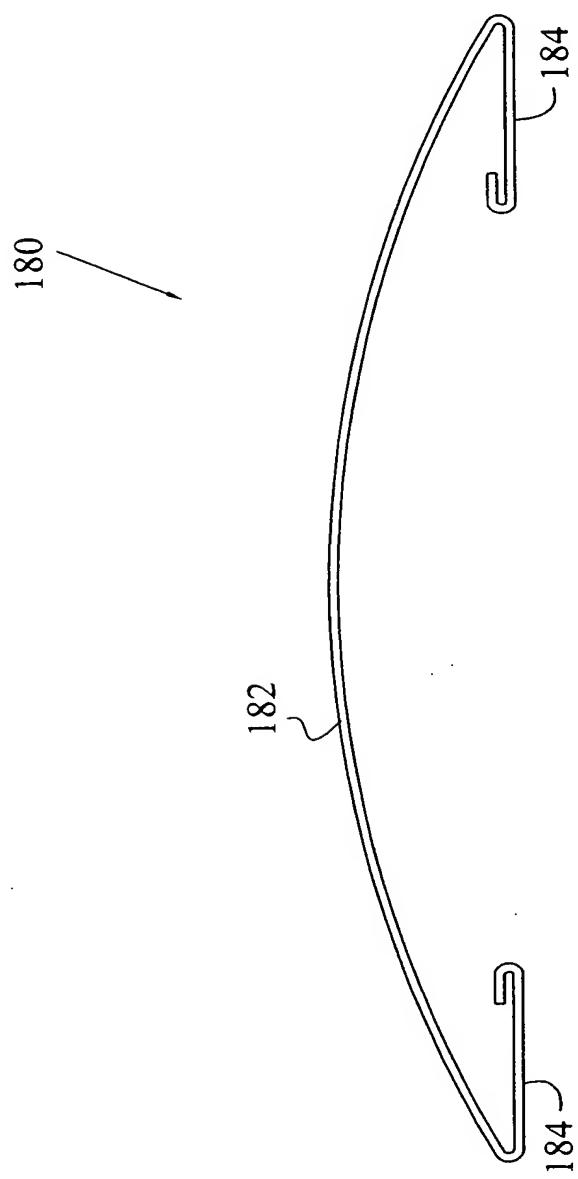


FIG. 15

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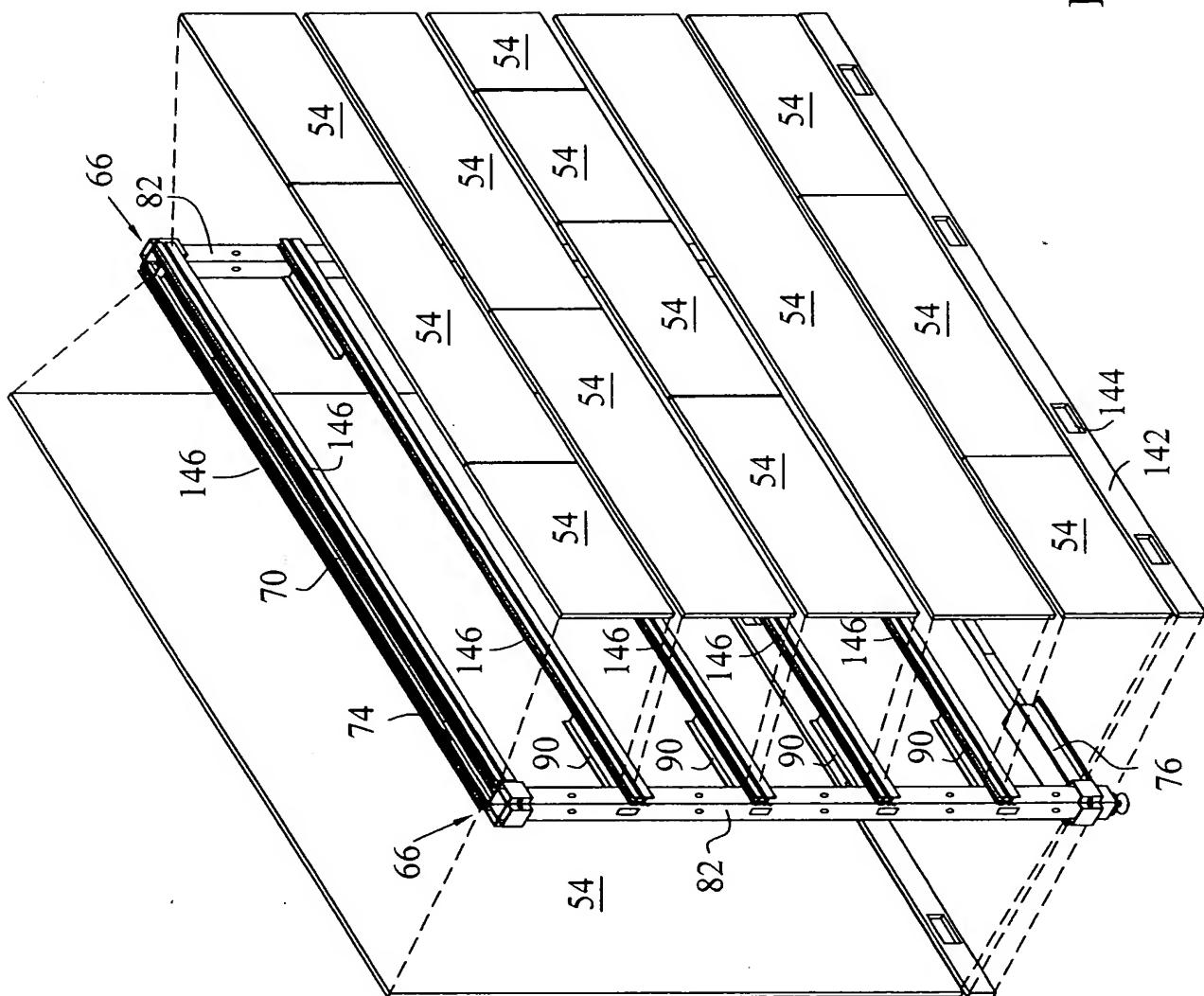


FIG. 16A

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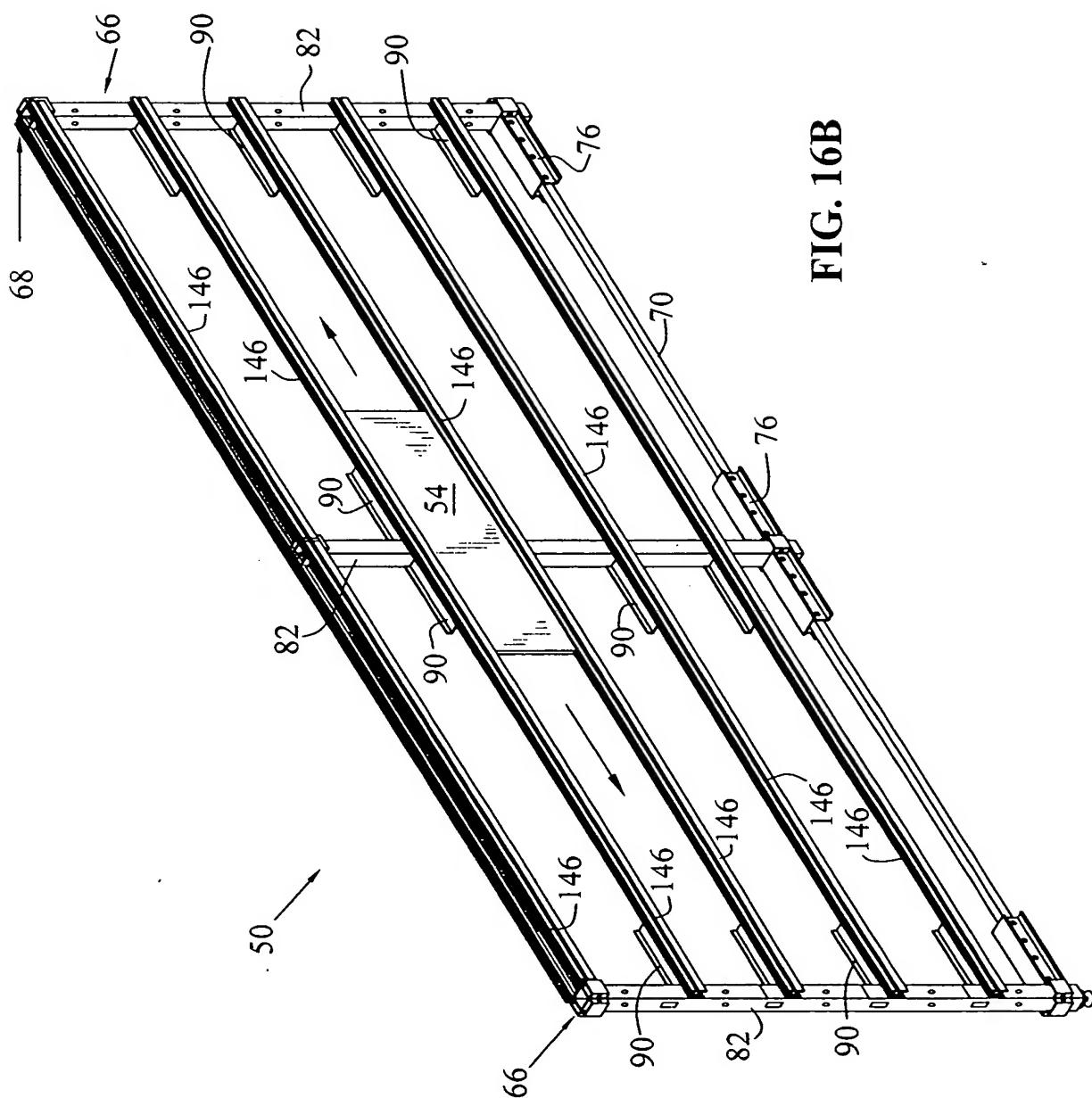


FIG. 16B

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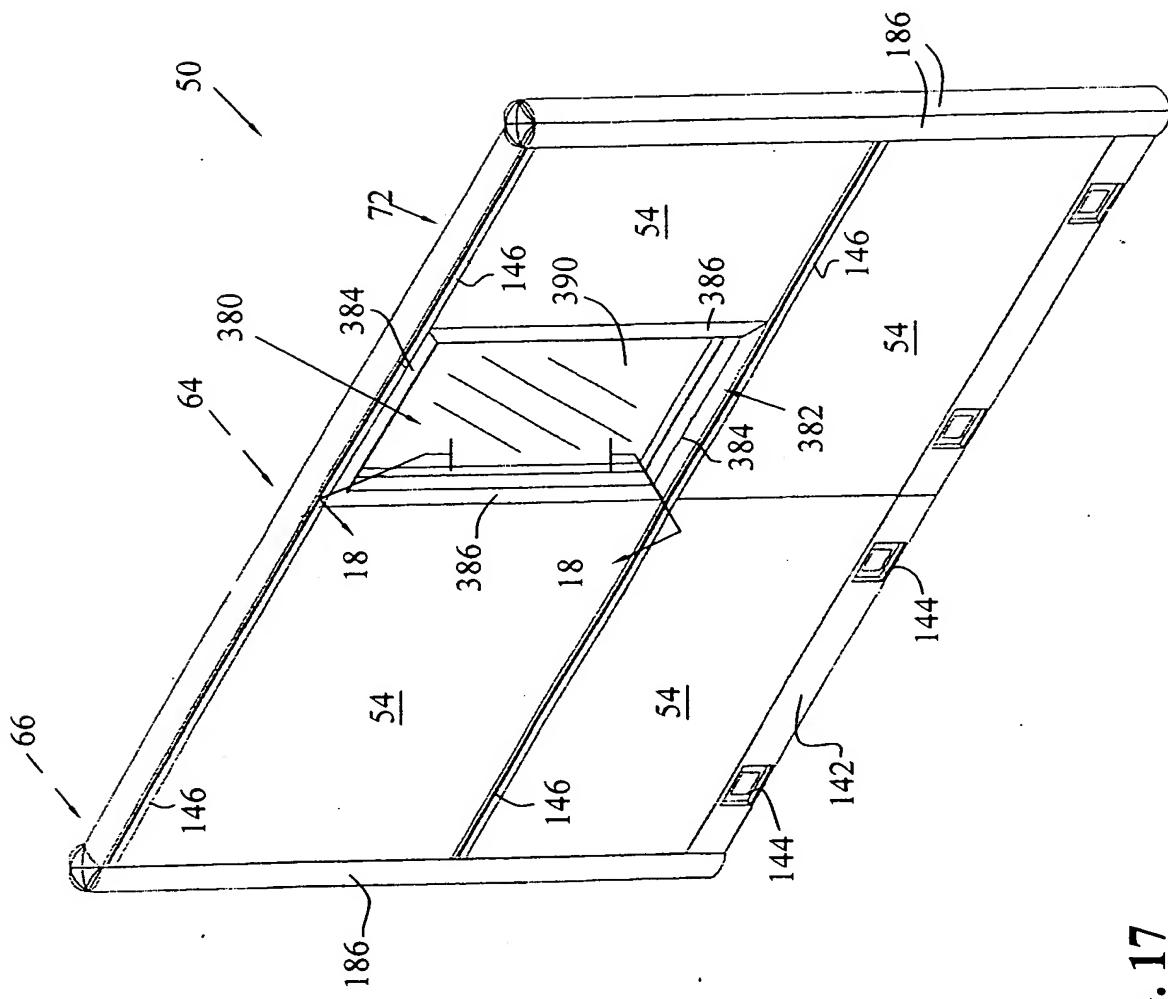


FIG. 17

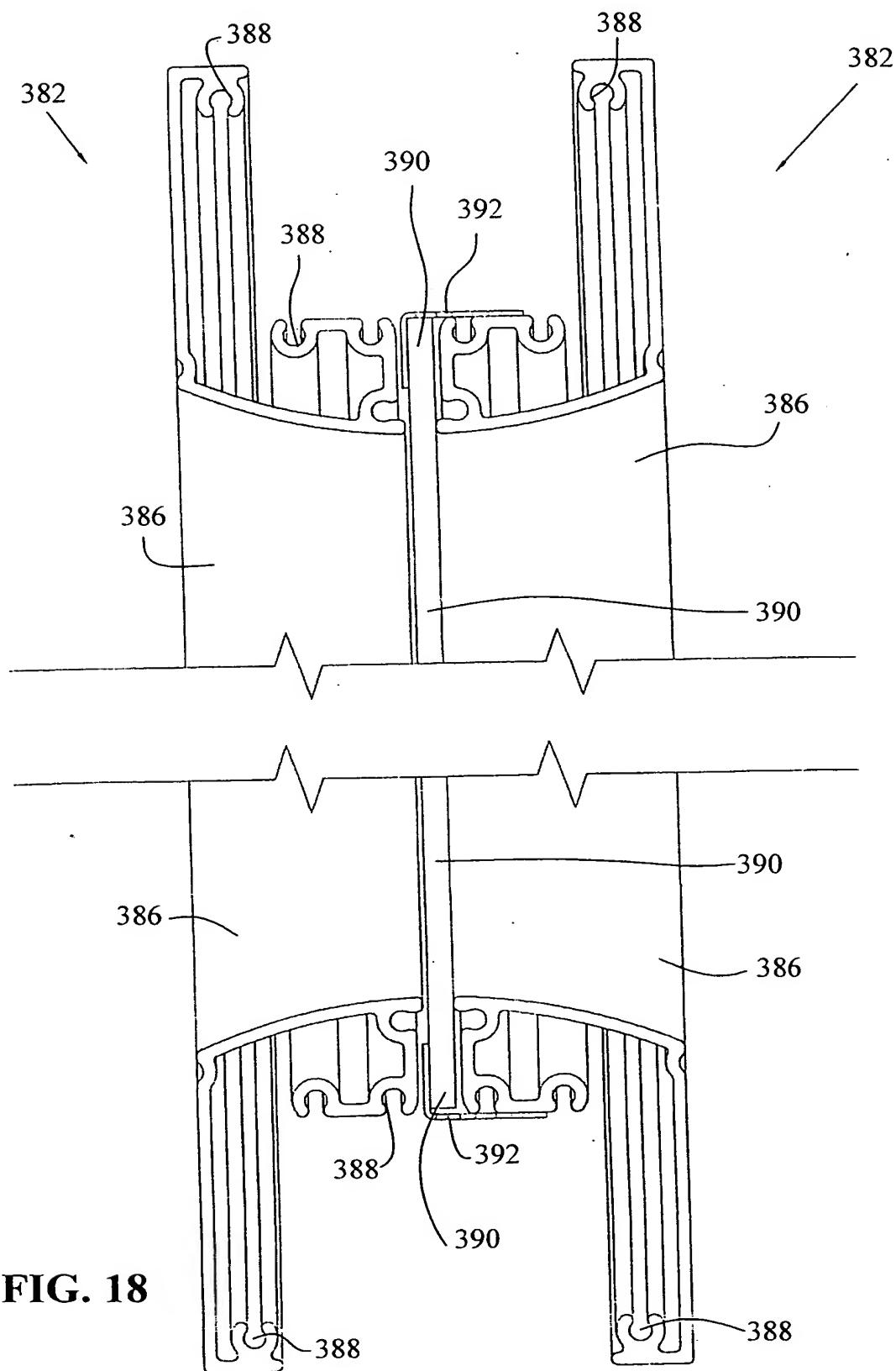


FIG. 18

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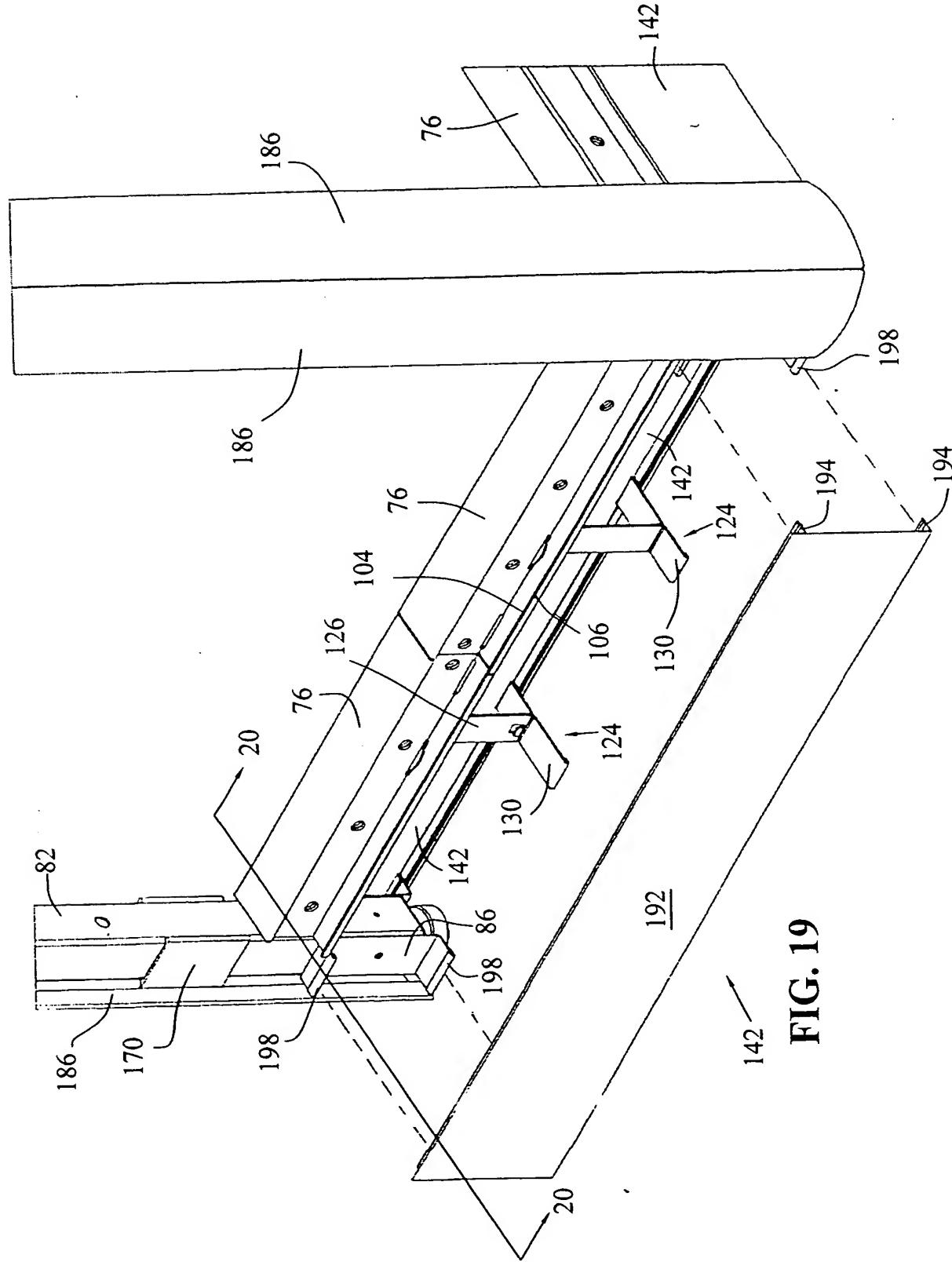


FIG. 19

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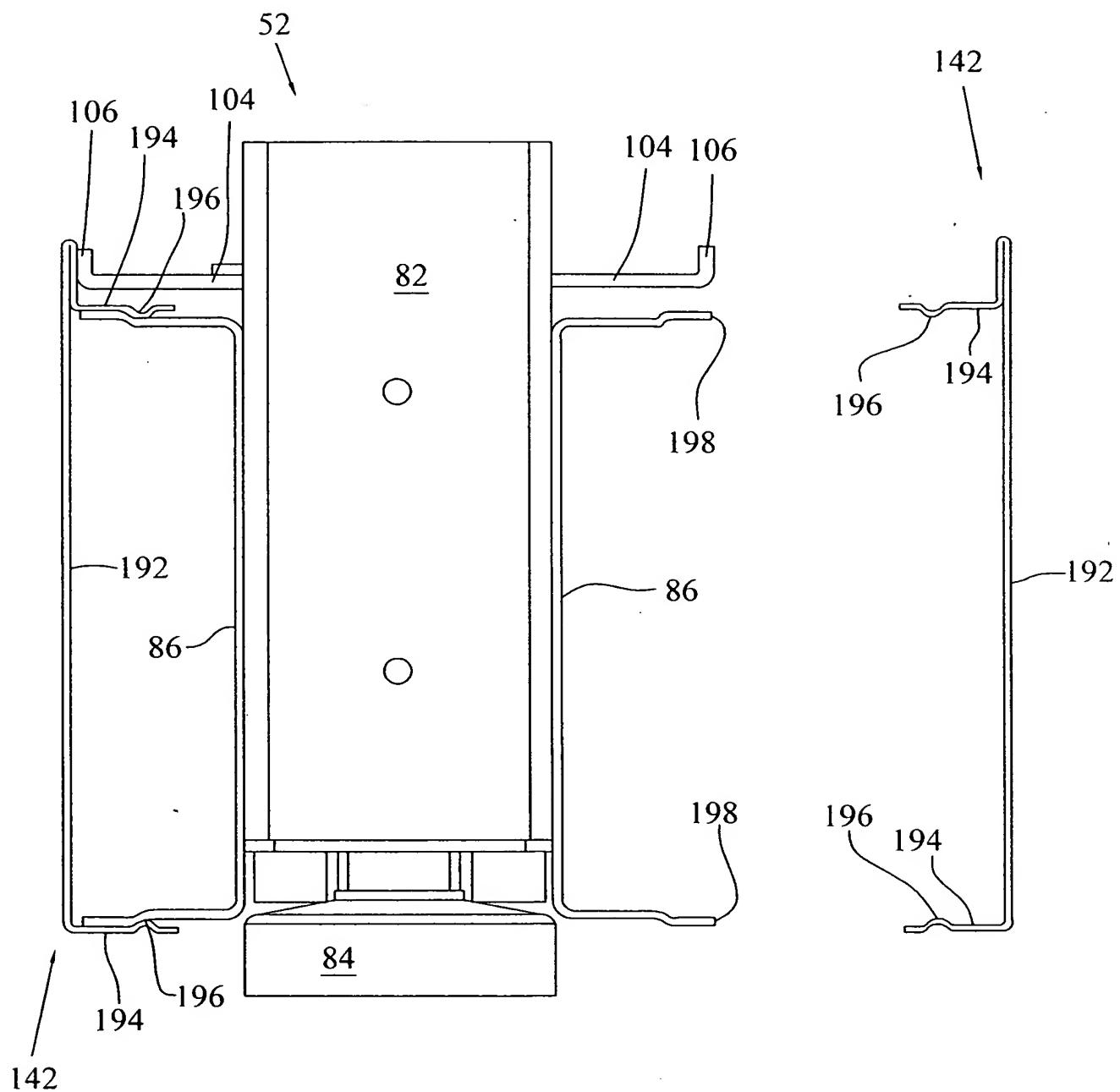


FIG. 20

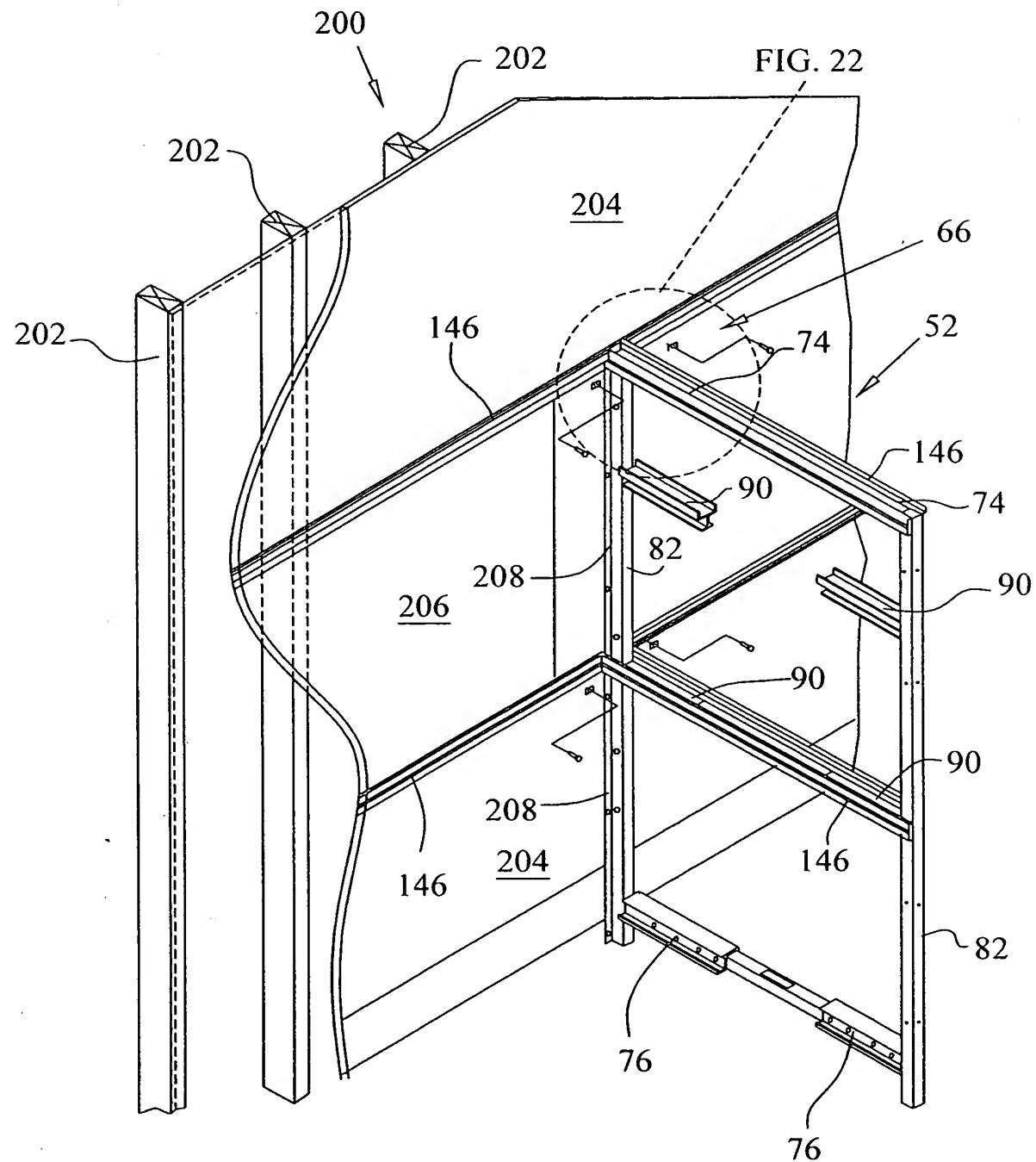


FIG. 21A

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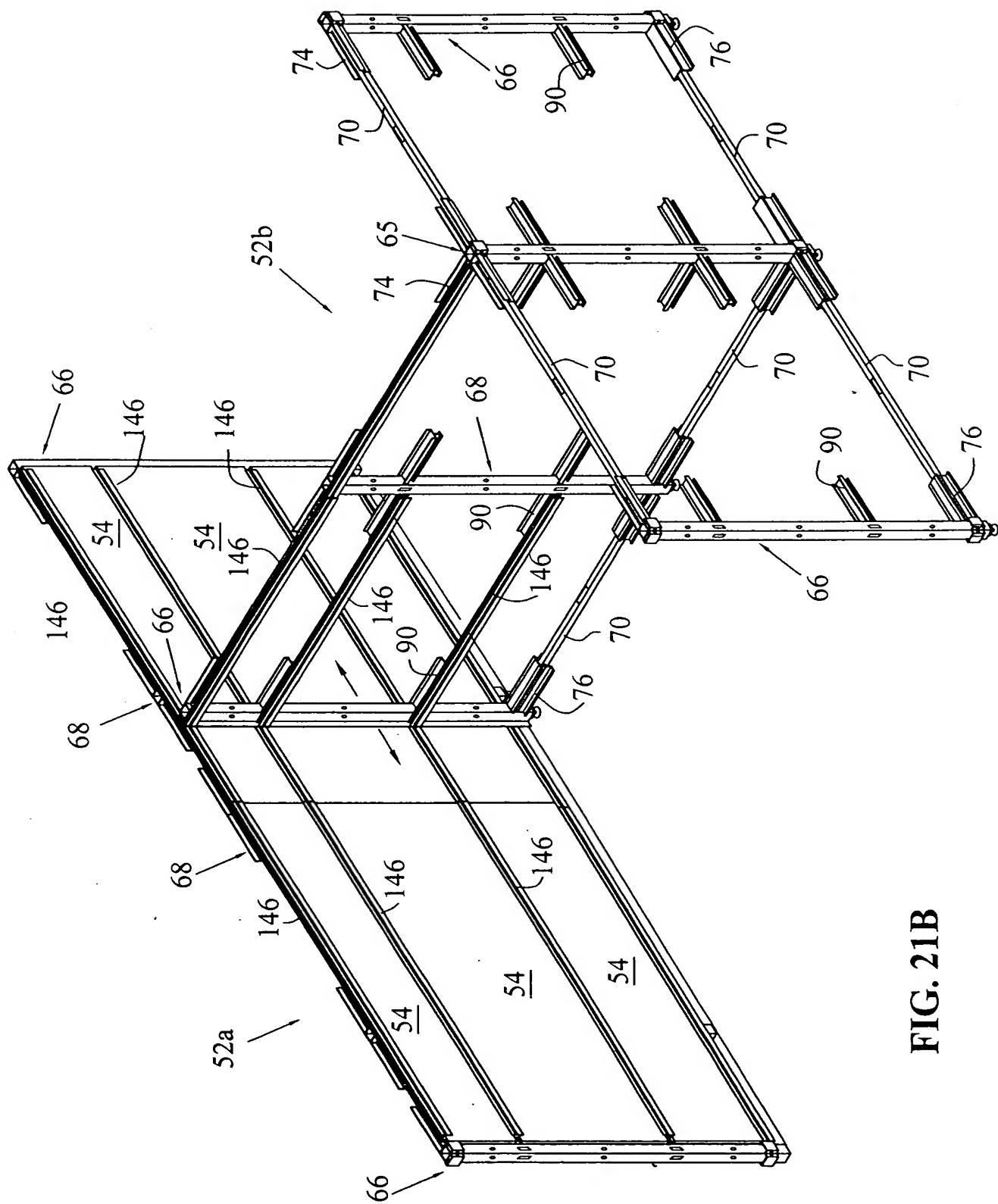


FIG. 21B

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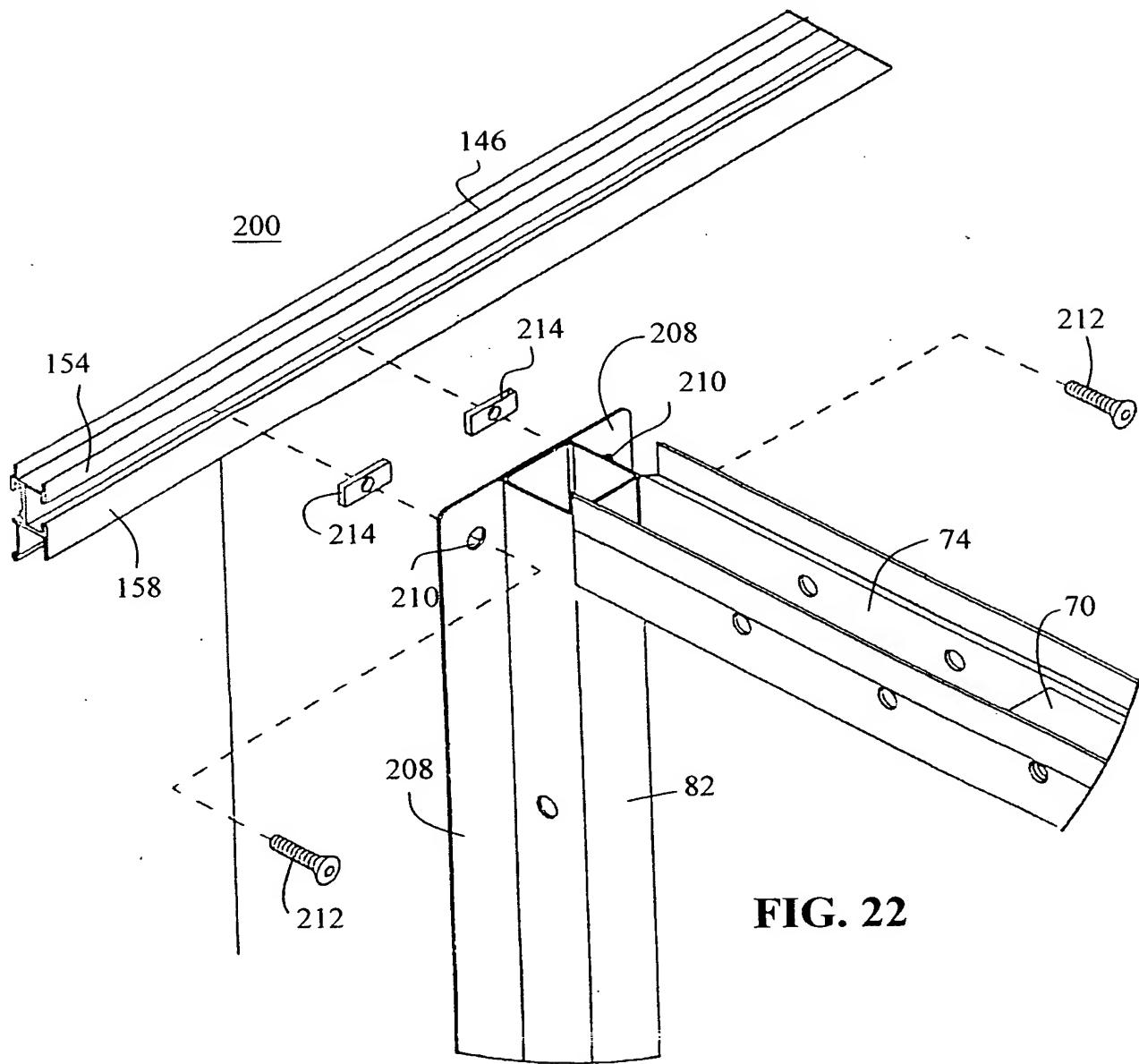
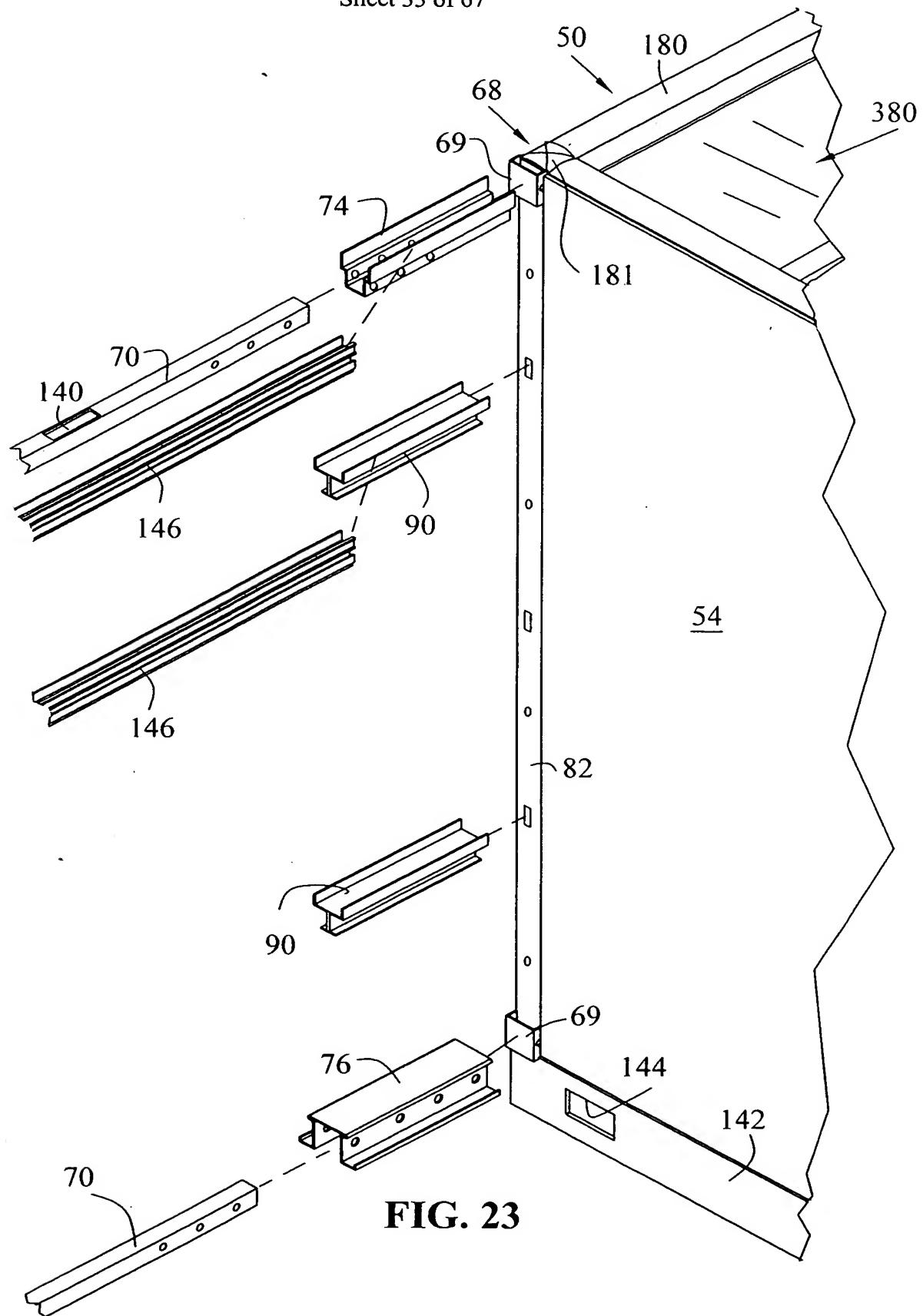


FIG. 22

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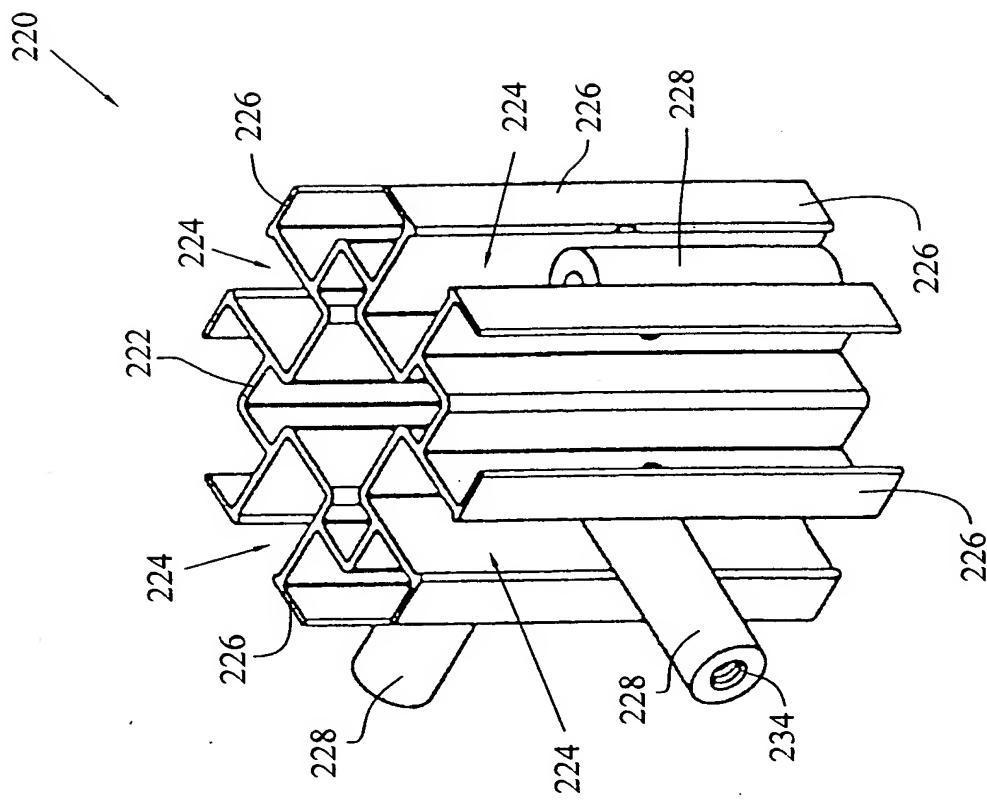


FIG. 24A

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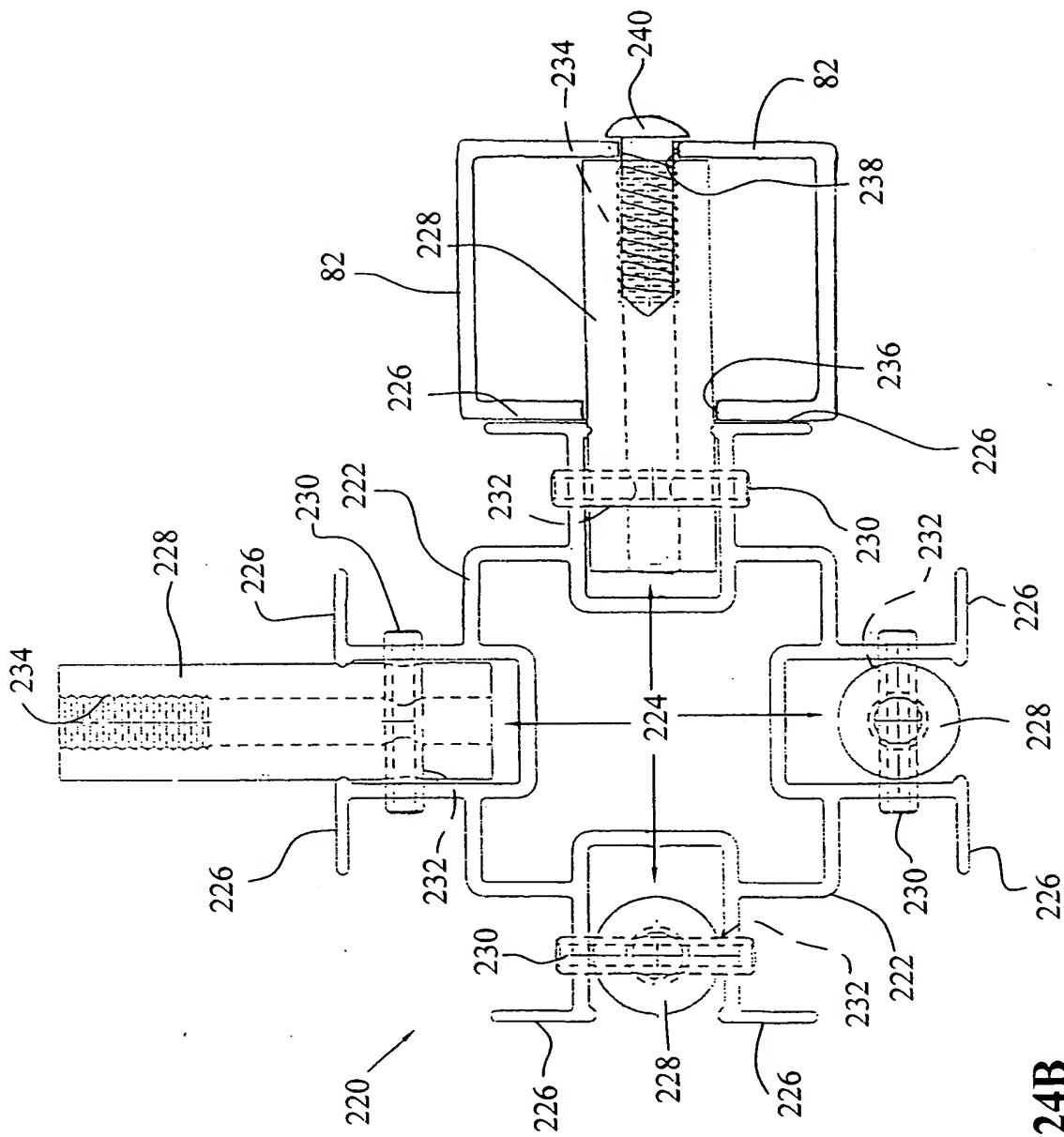


FIG. 24B

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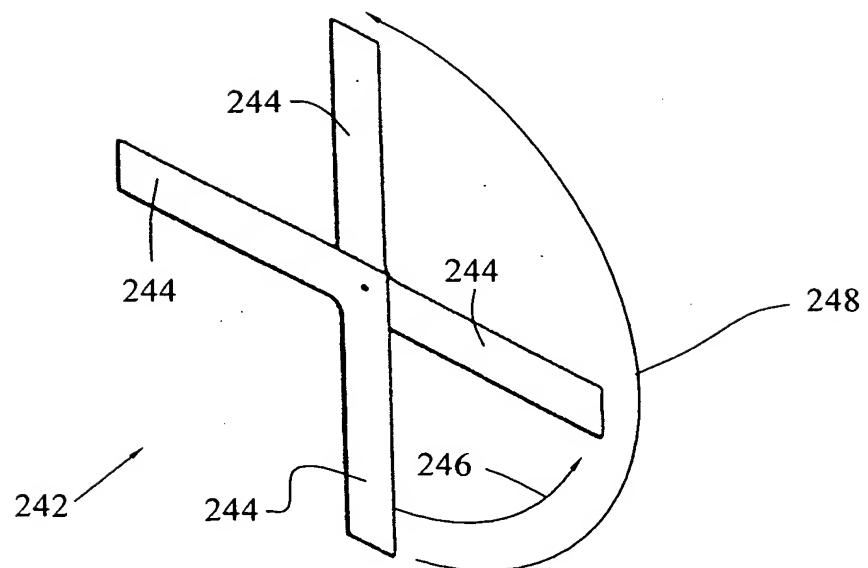


FIG. 25

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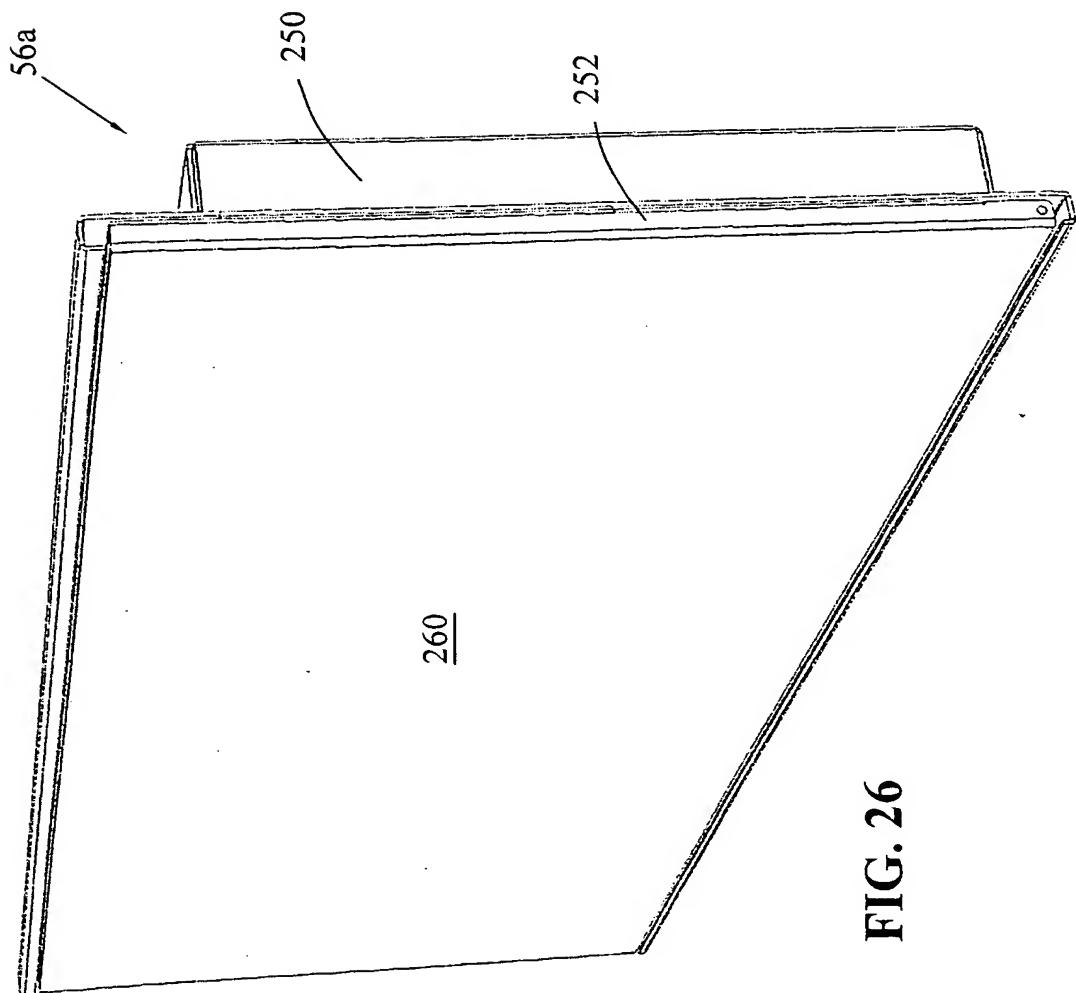


FIG. 26

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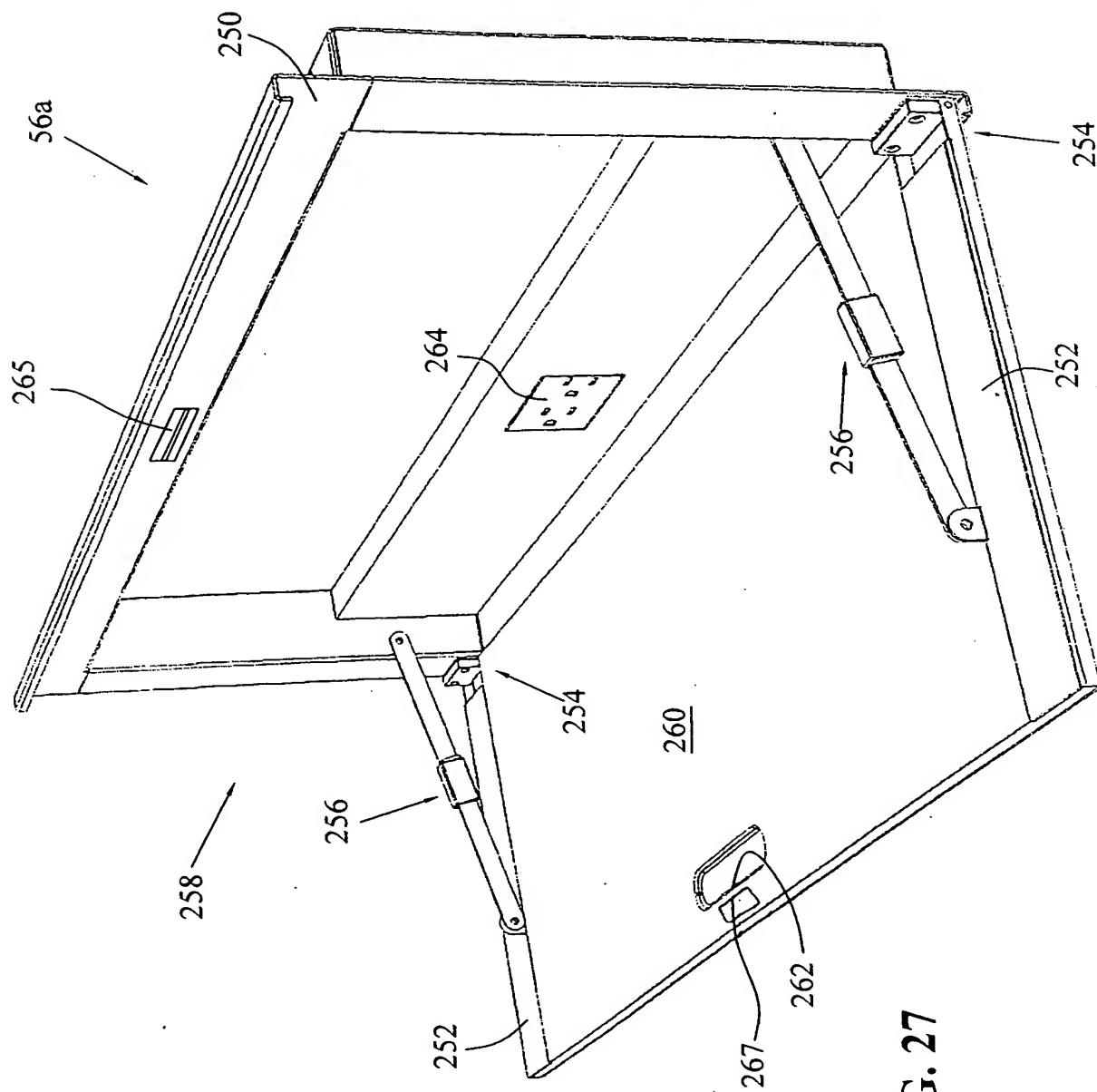
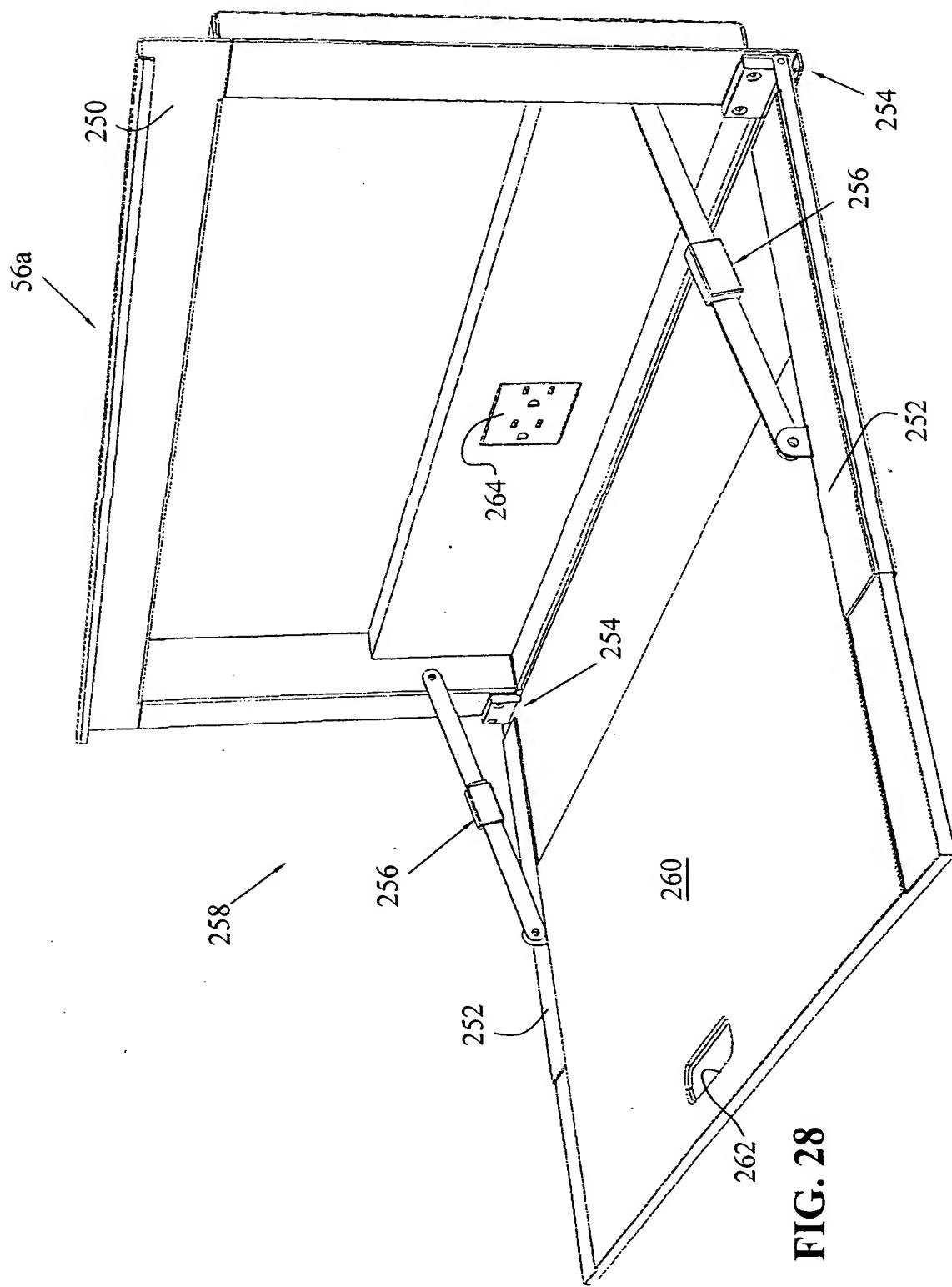


FIG. 27

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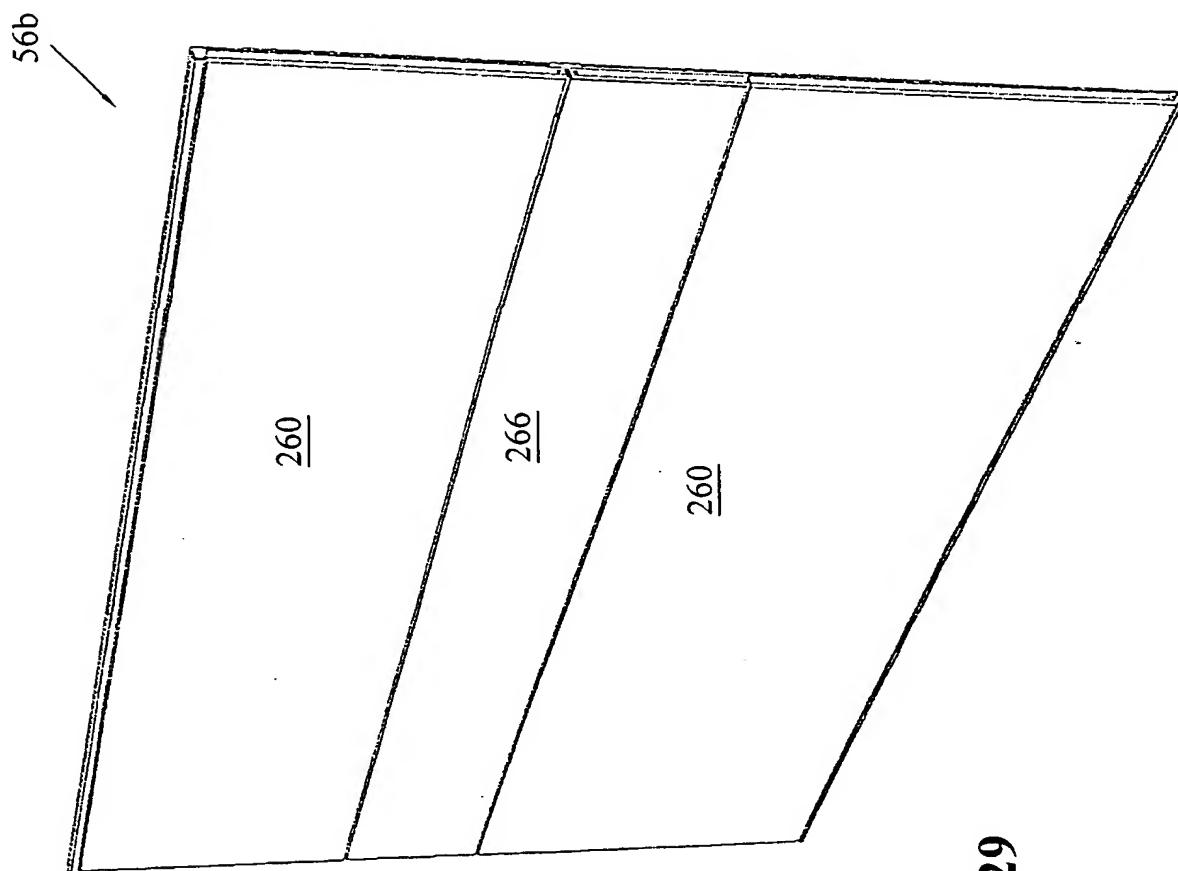


FIG. 29

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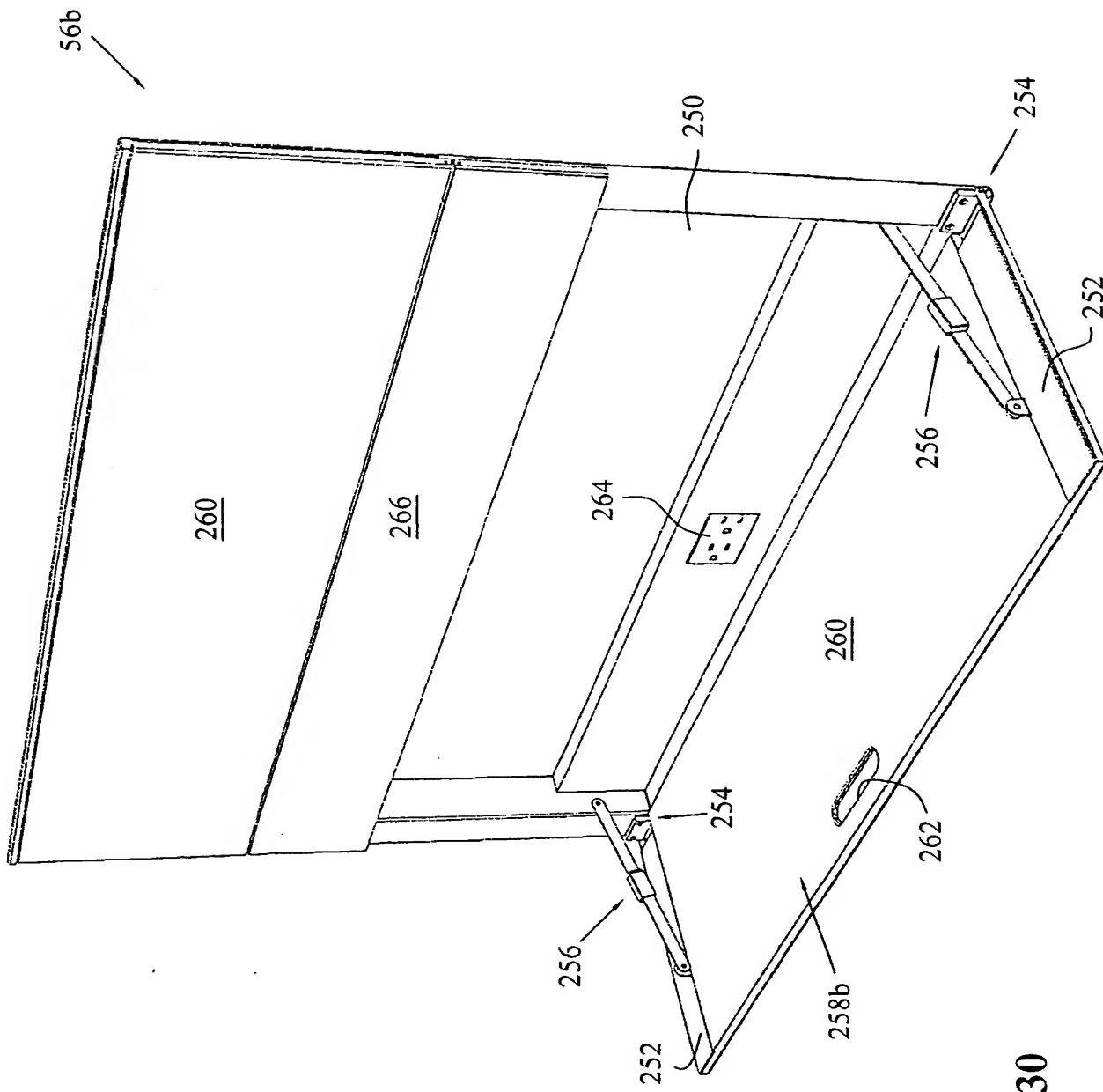


FIG. 30

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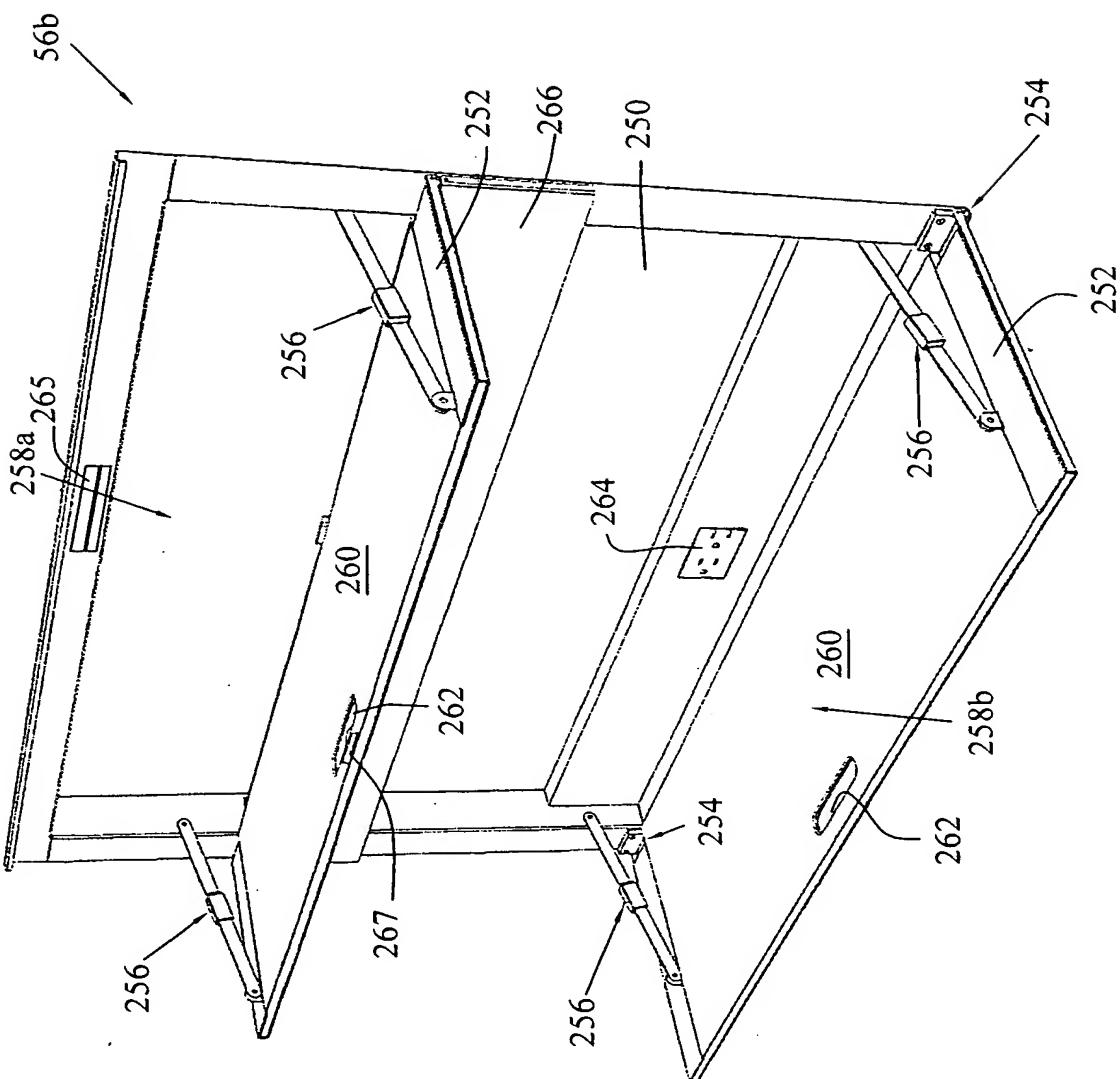


FIG. 31

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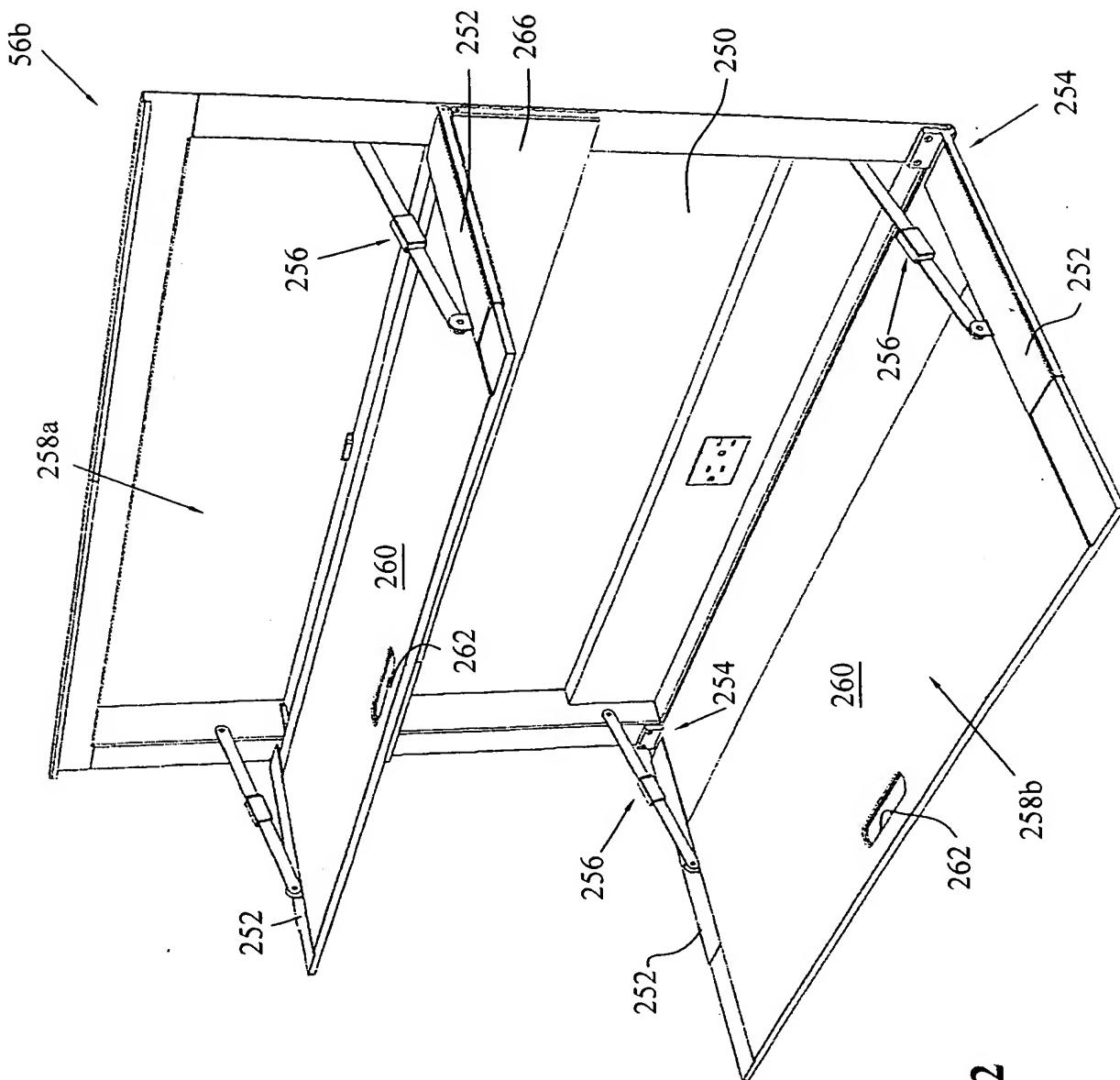


FIG. 32

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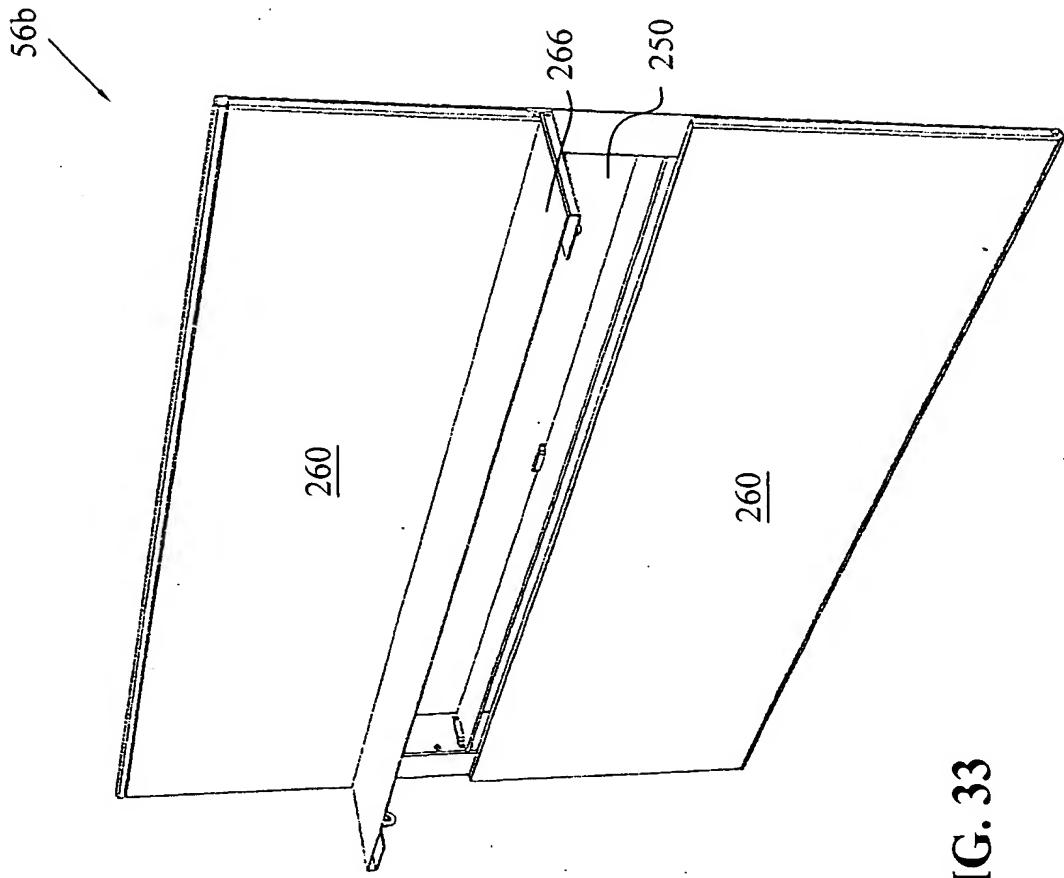


FIG. 33

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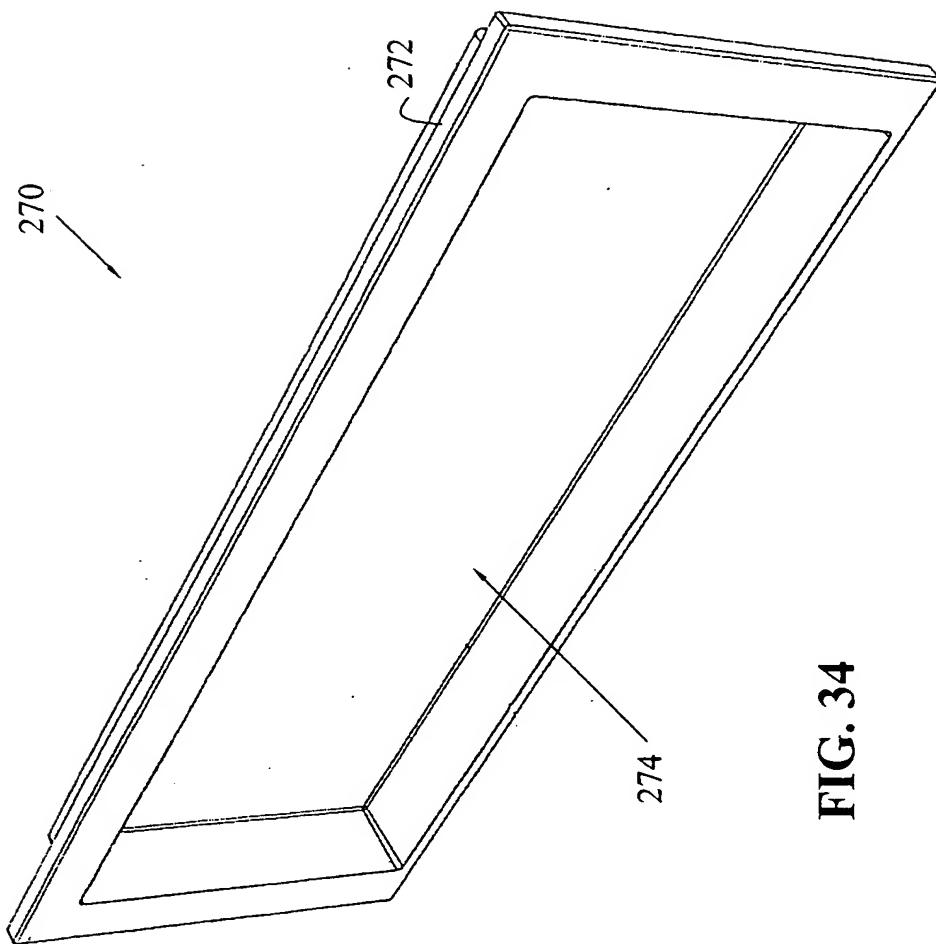


FIG. 34

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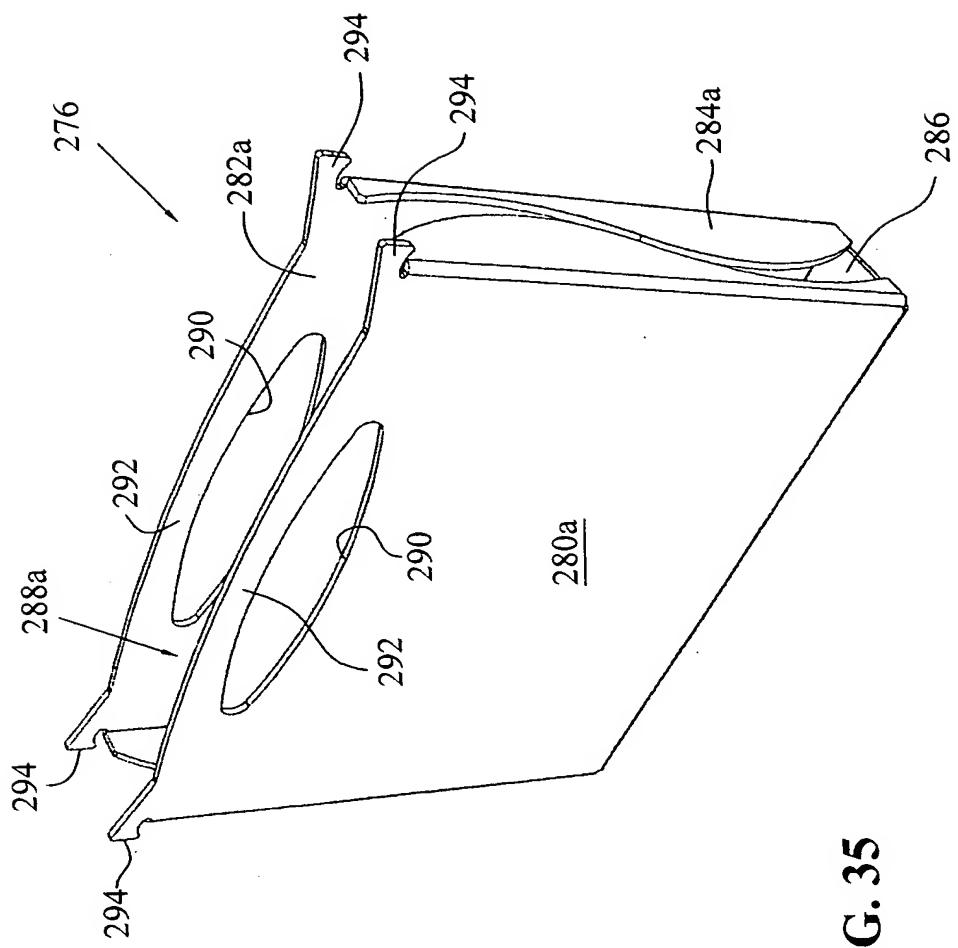


FIG. 35

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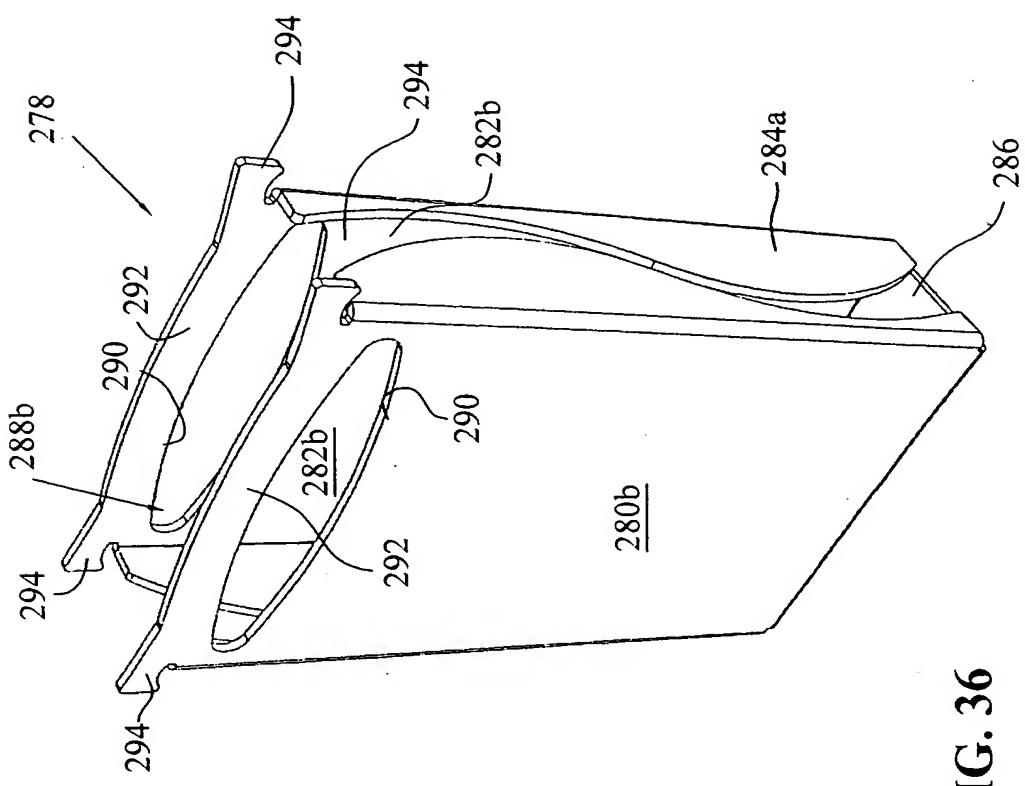


FIG. 36

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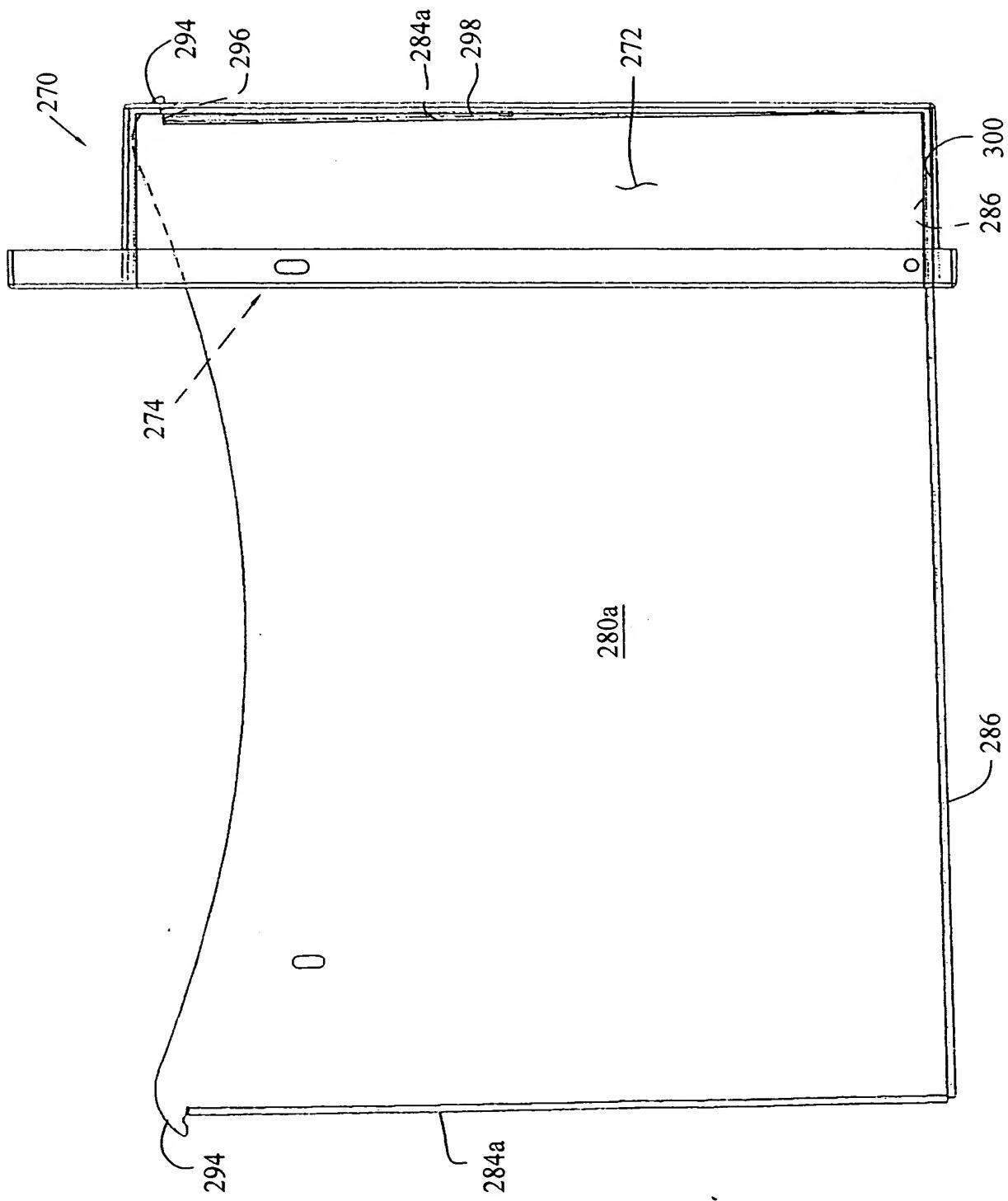


FIG. 37

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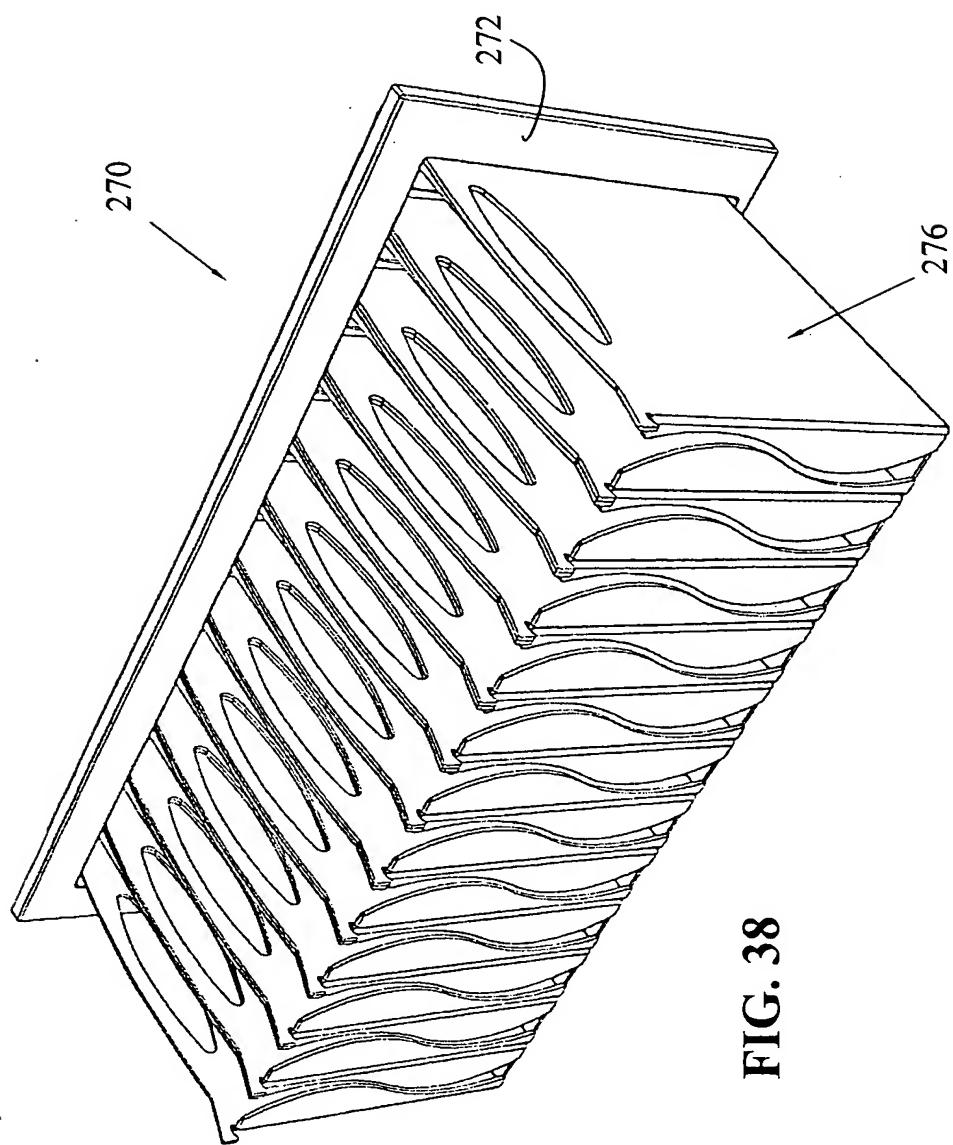


FIG. 38

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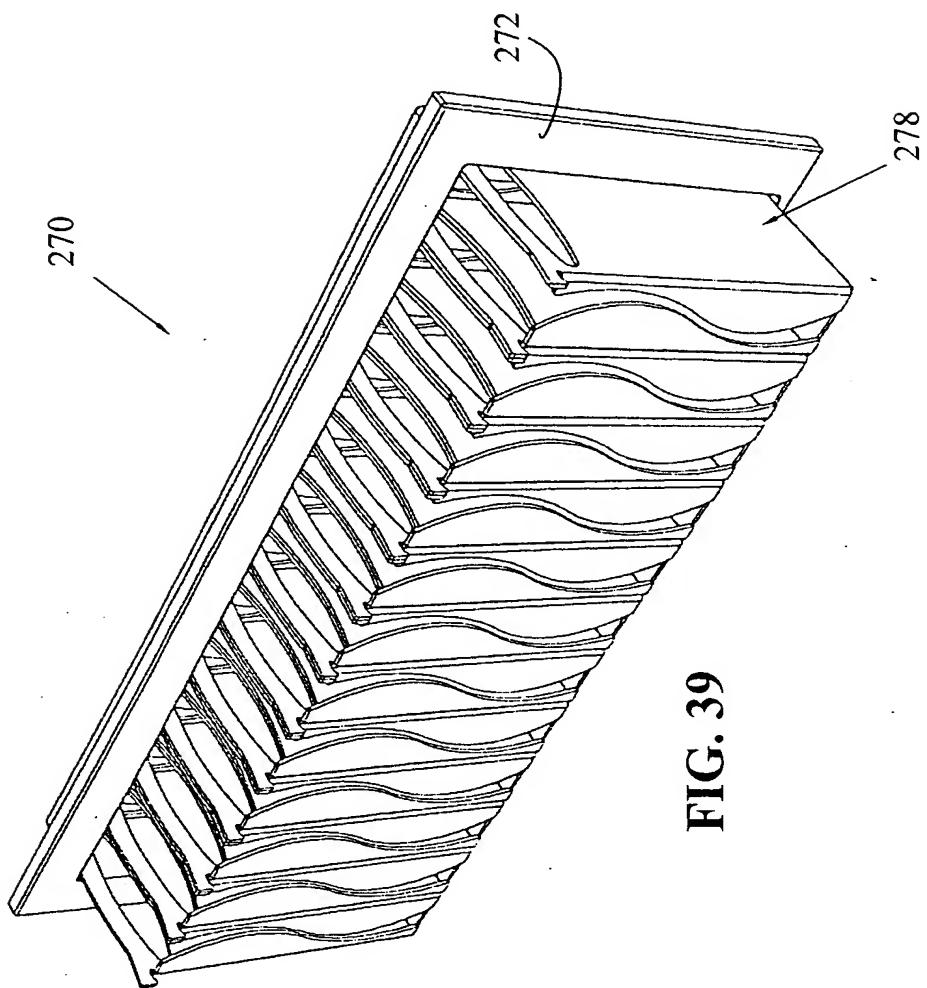


FIG. 39

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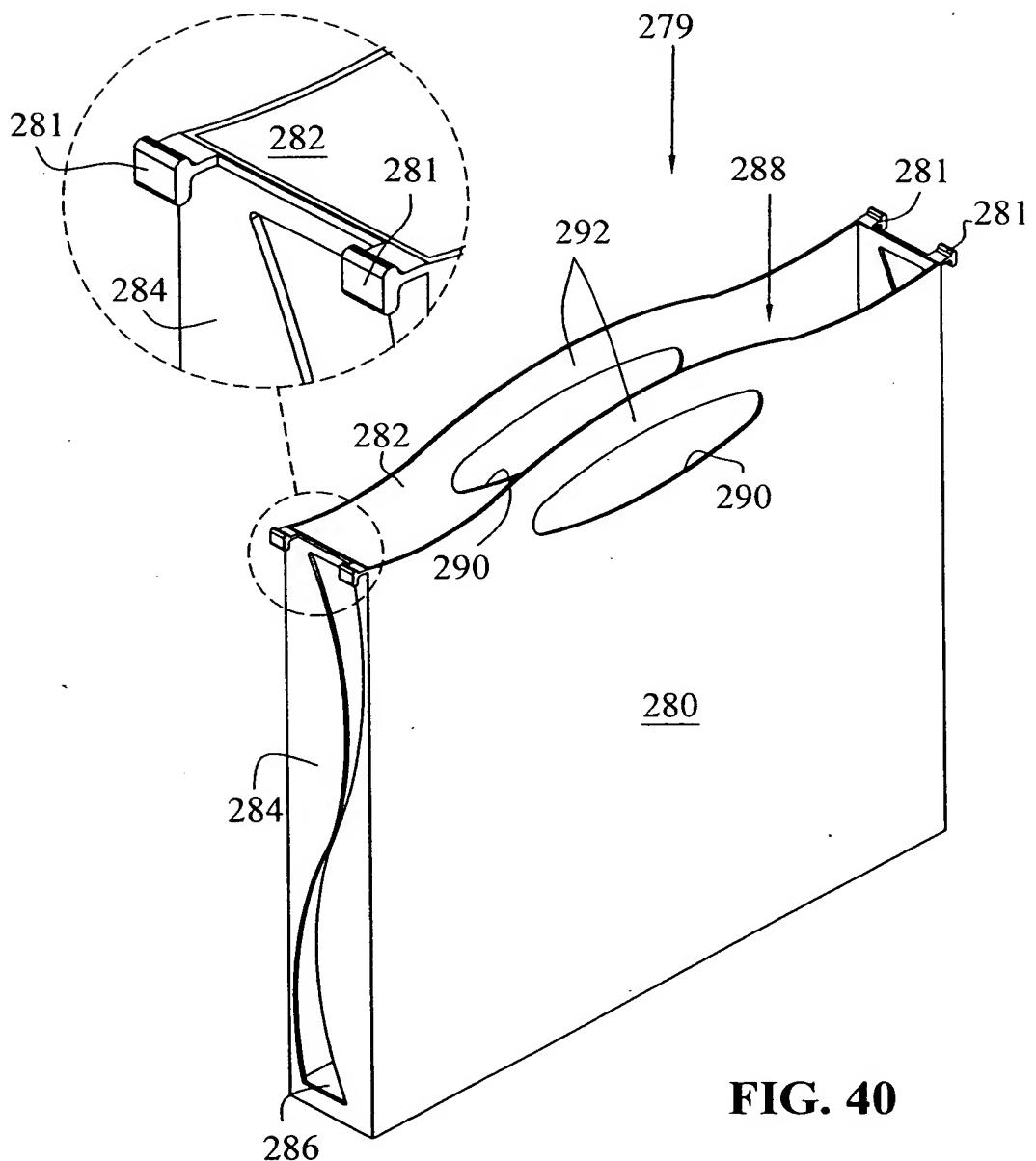
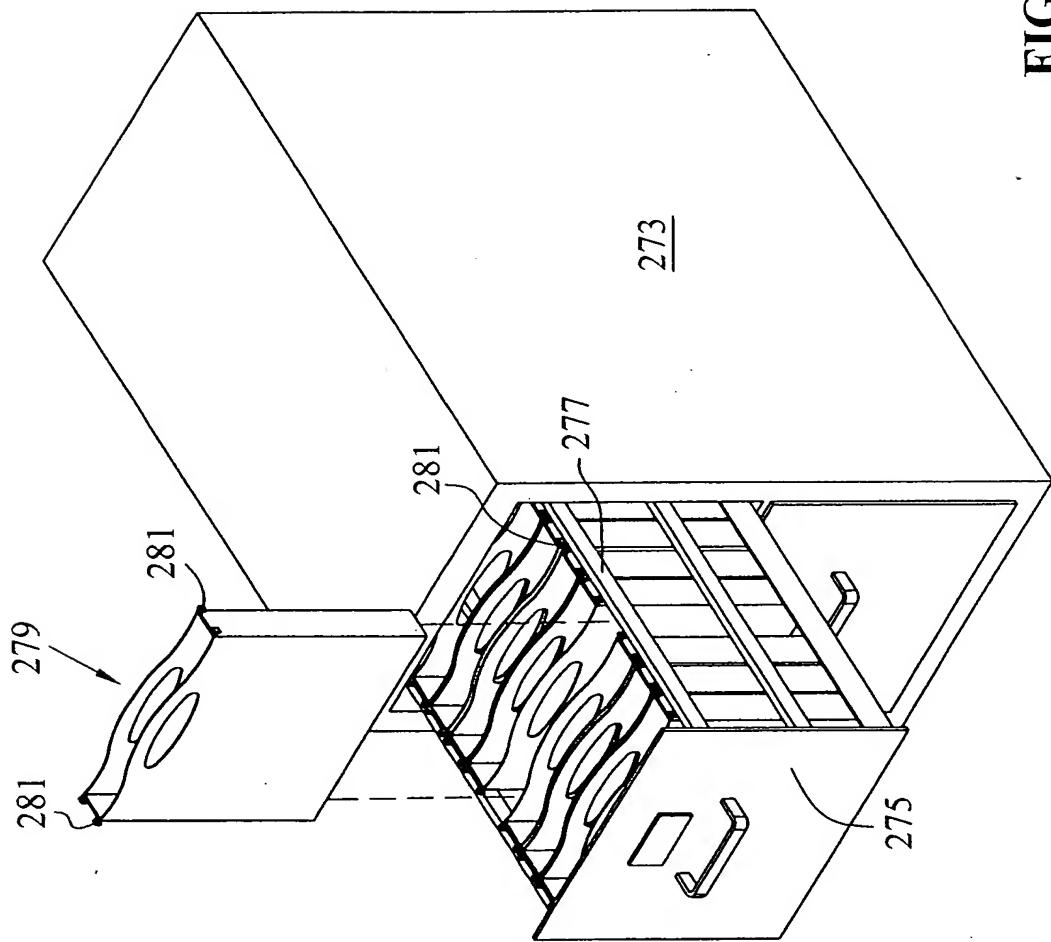


FIG. 40

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FIG. 41



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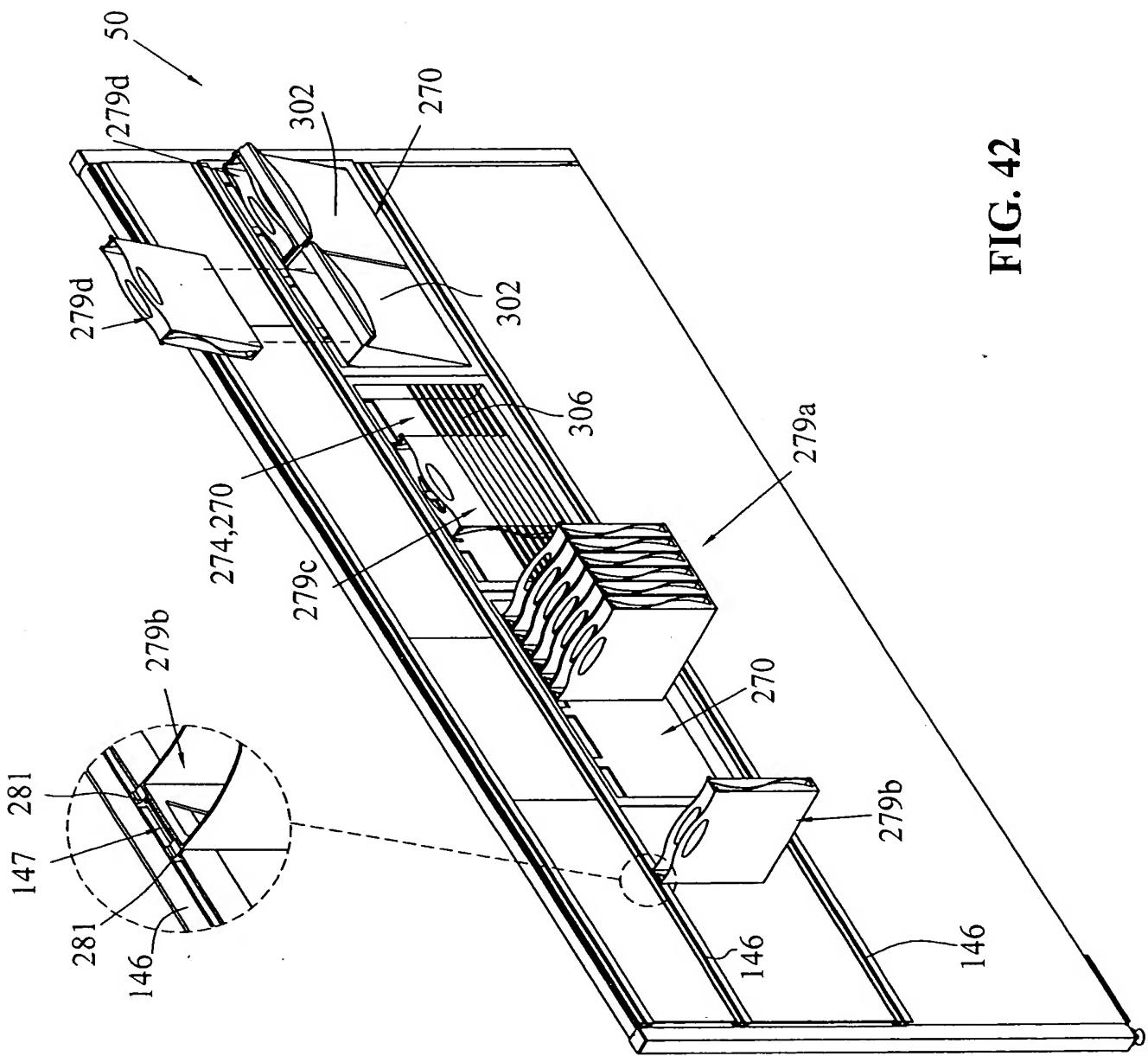


FIG. 42

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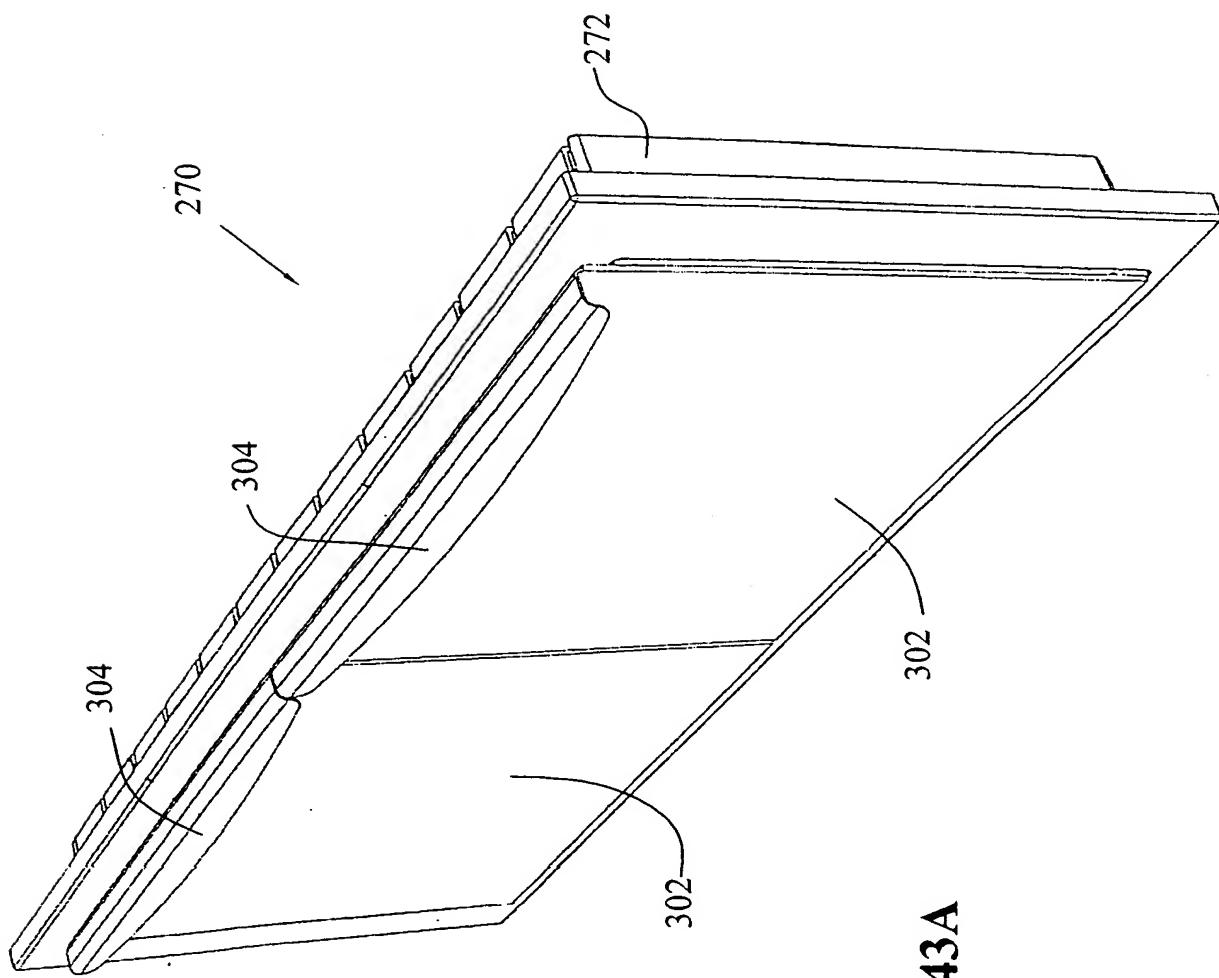


FIG. 43A

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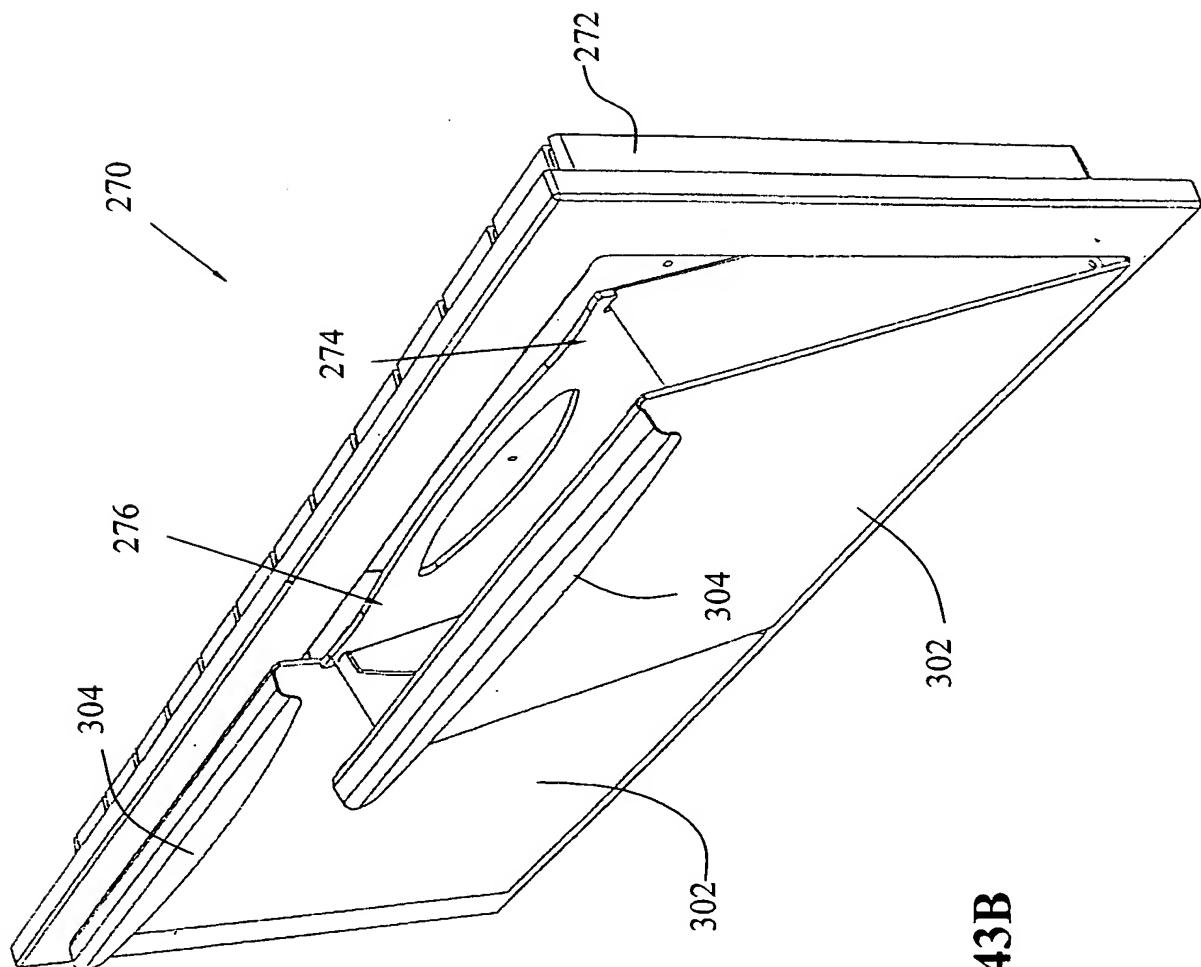
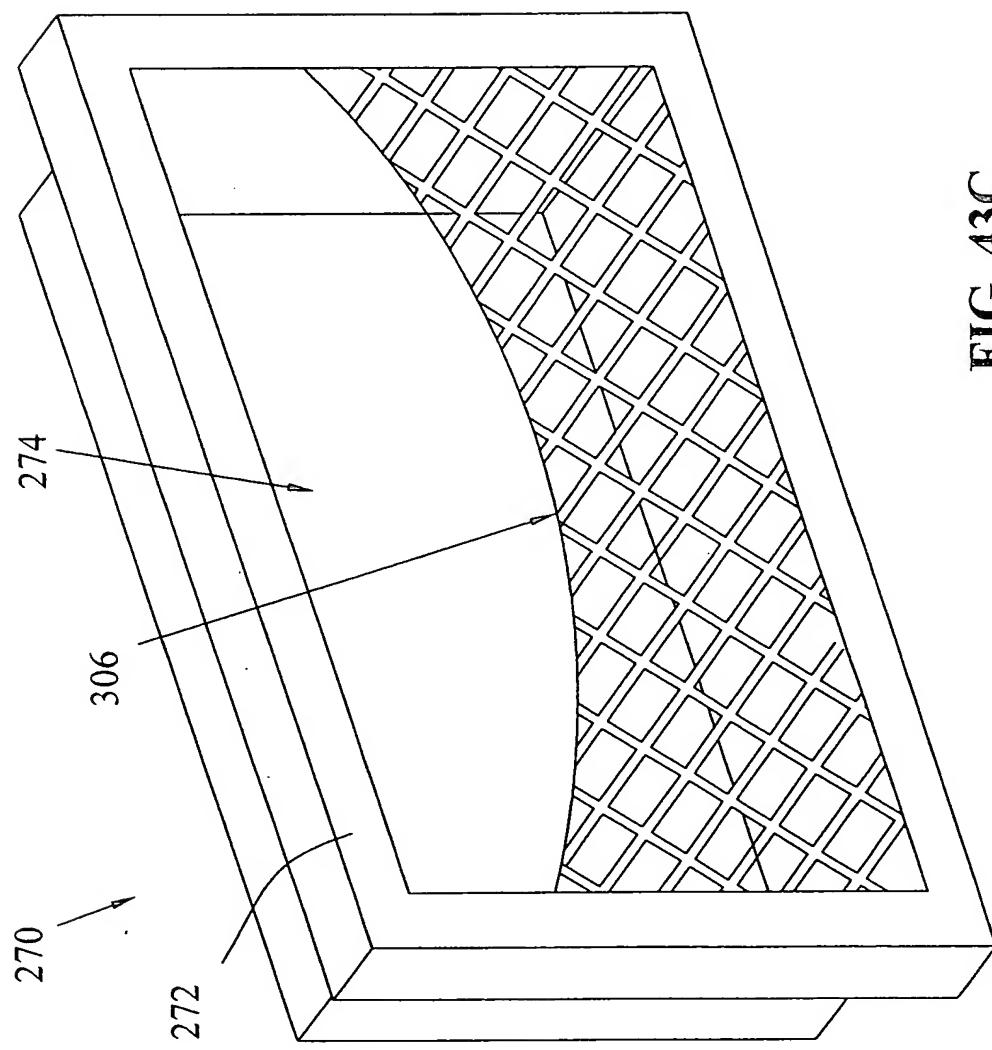


FIG. 43B

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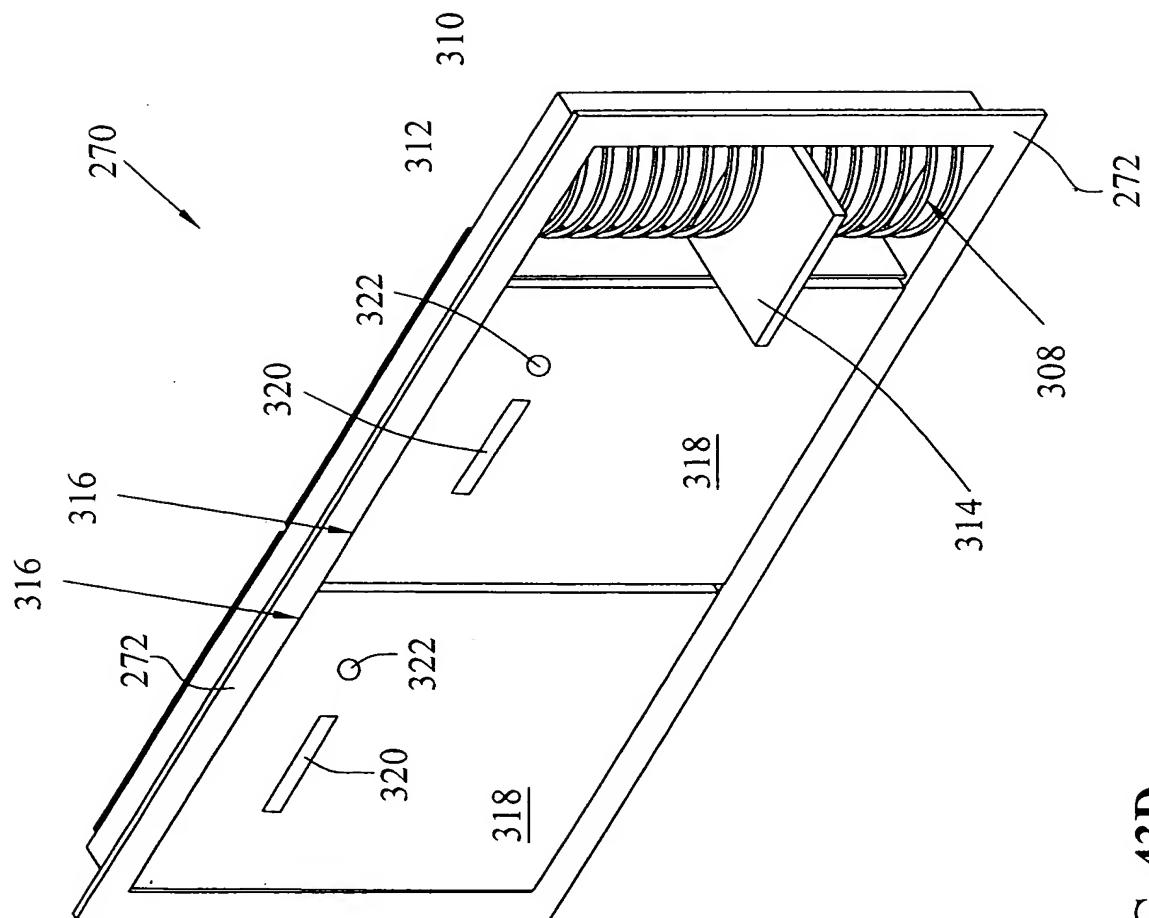


FIG. 43D

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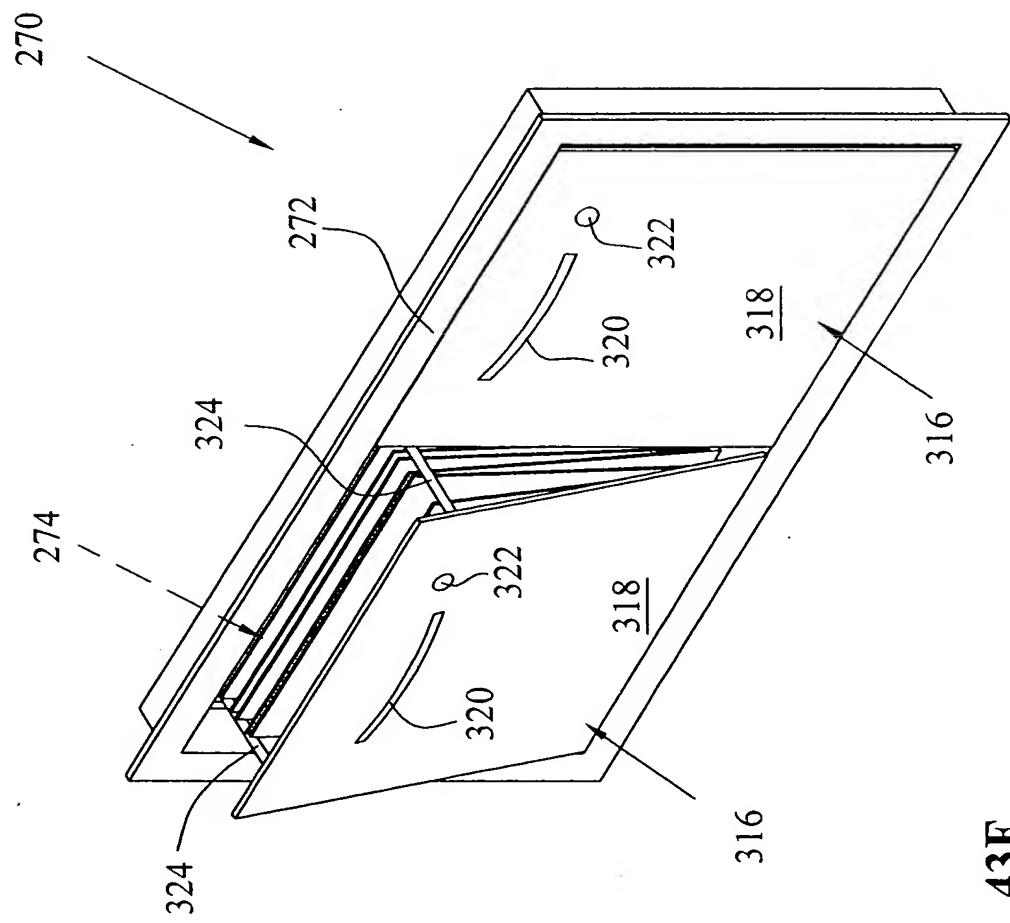


FIG. 43E

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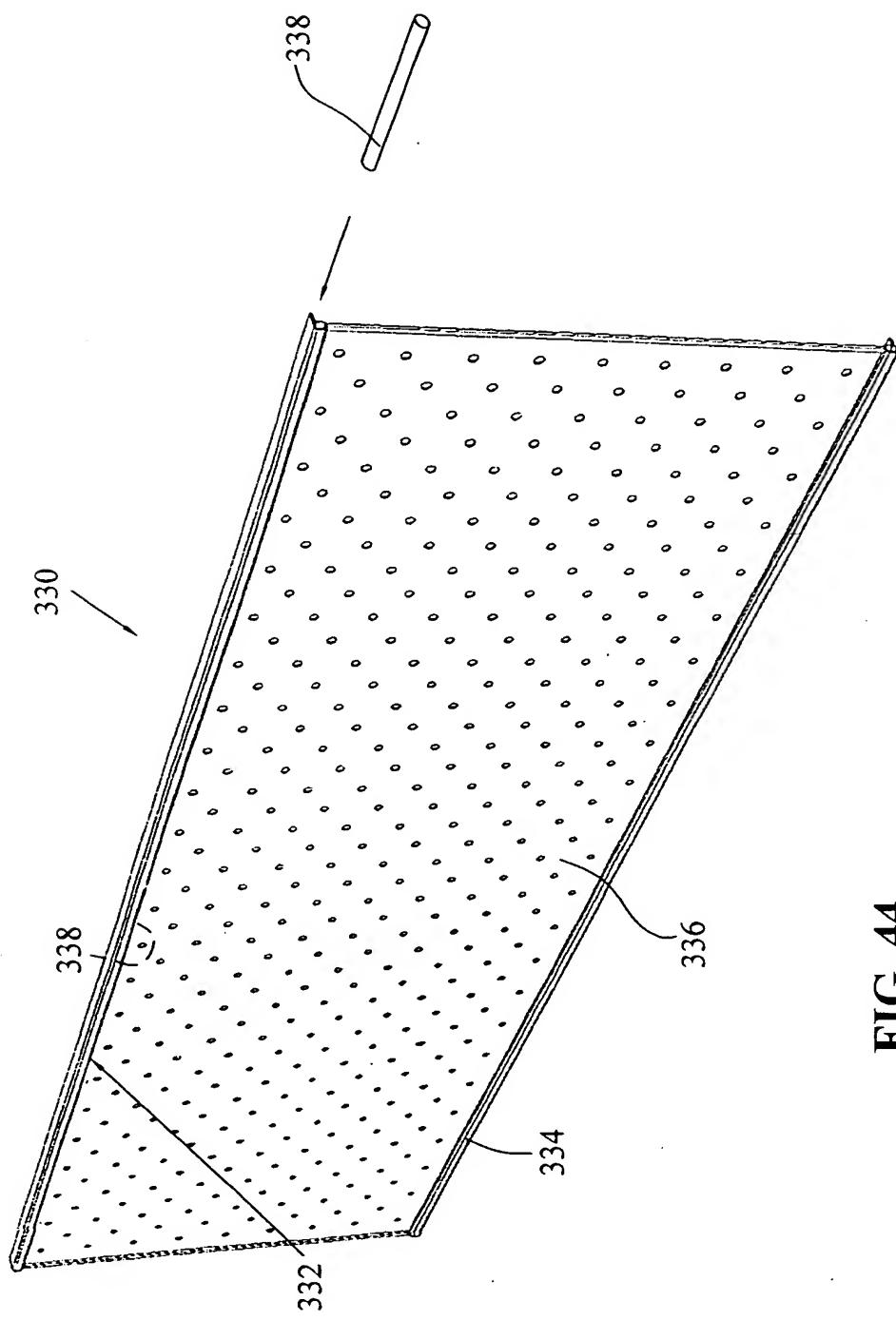


FIG. 44

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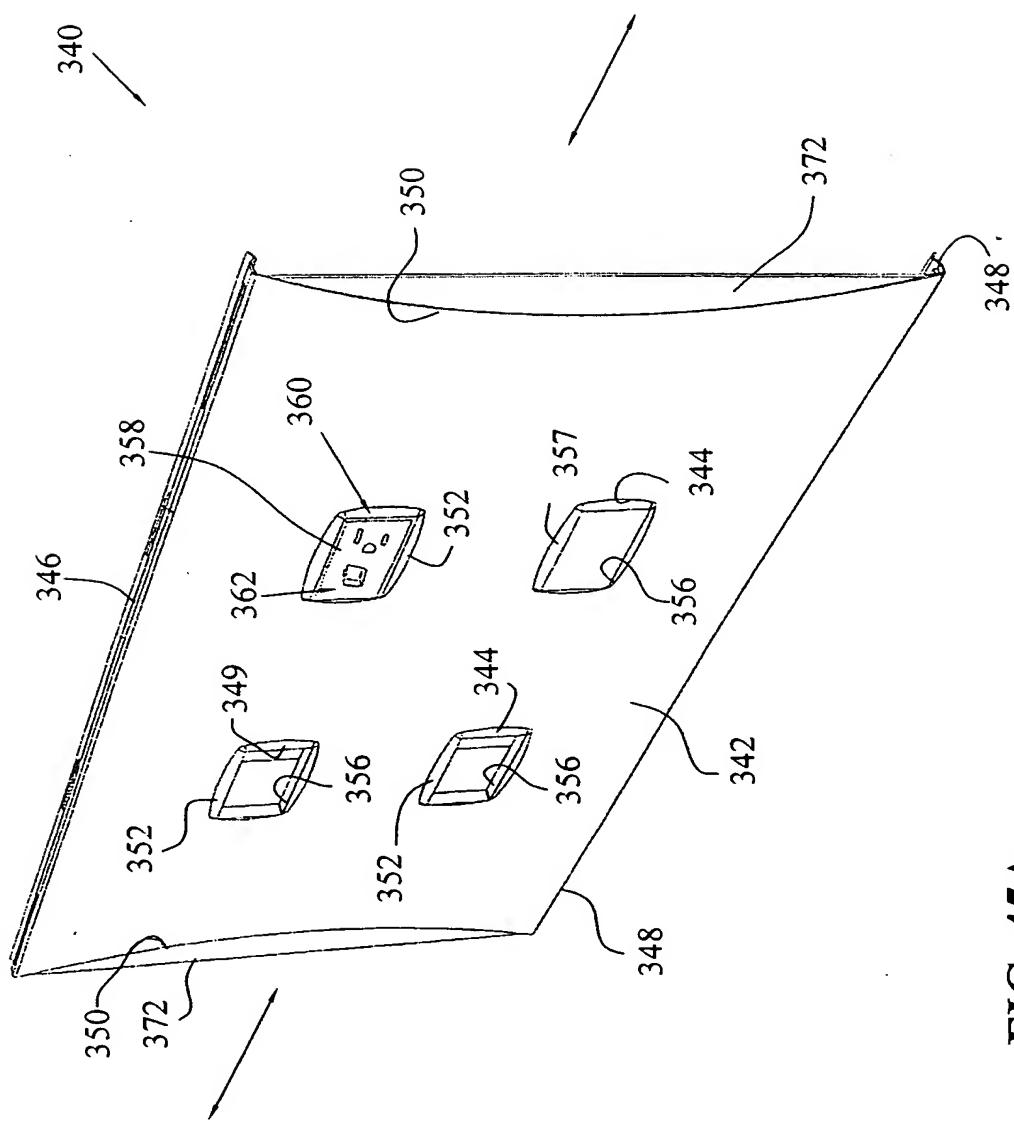


FIG. 45A

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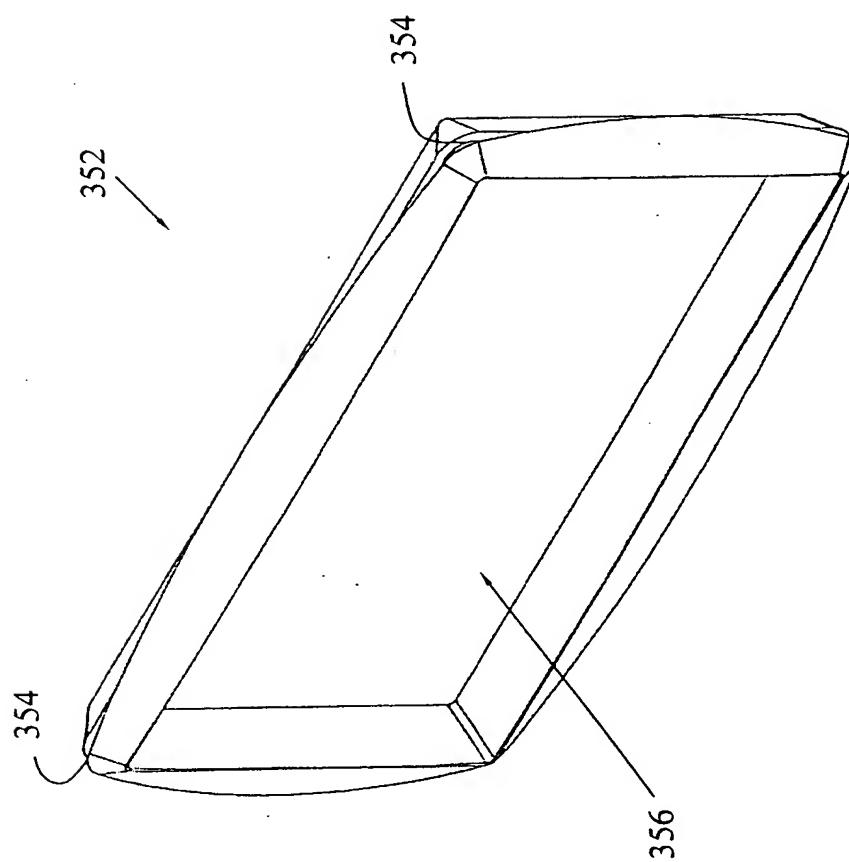


FIG. 45B

PARTITION SYSTEM
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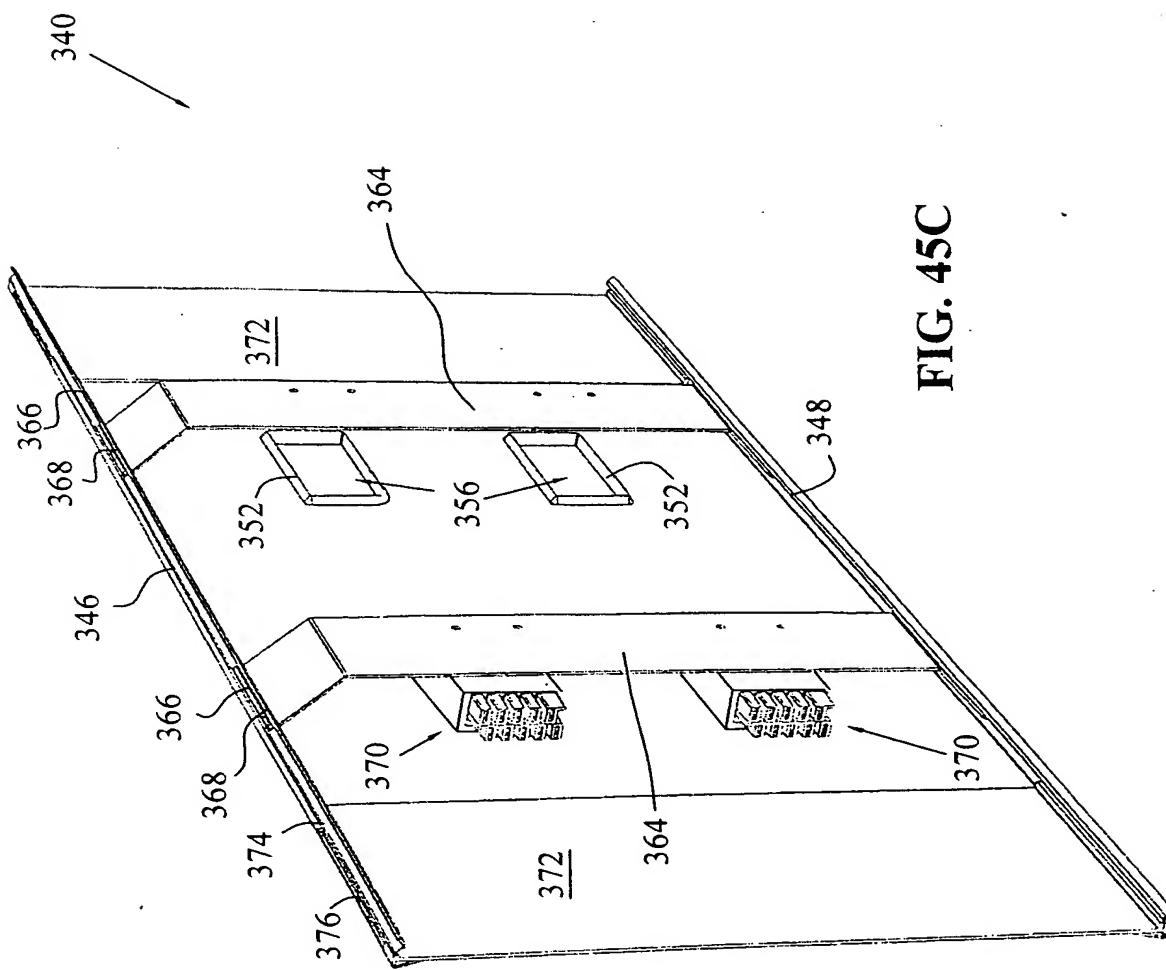


FIG. 45C

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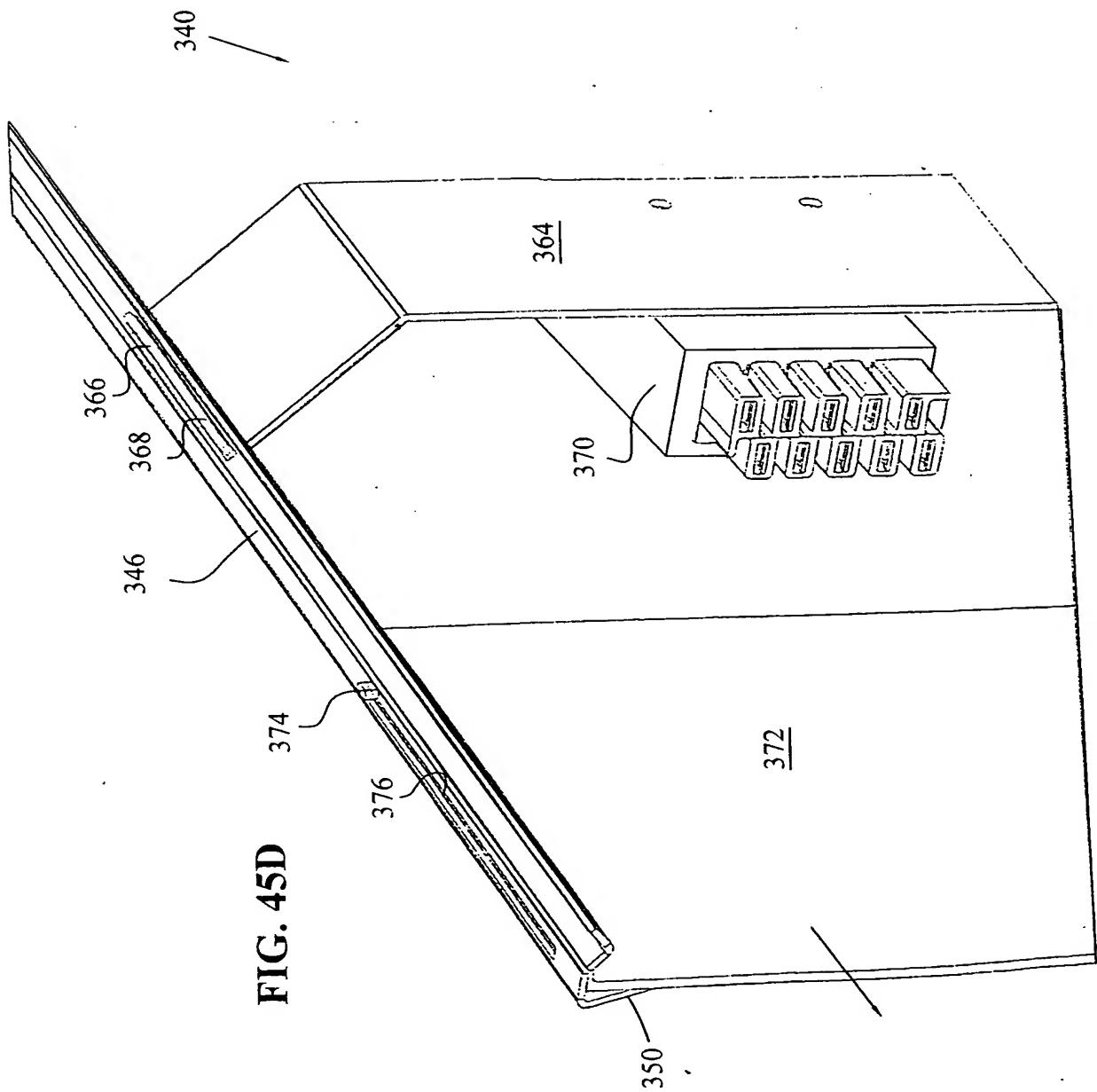


FIG. 45D

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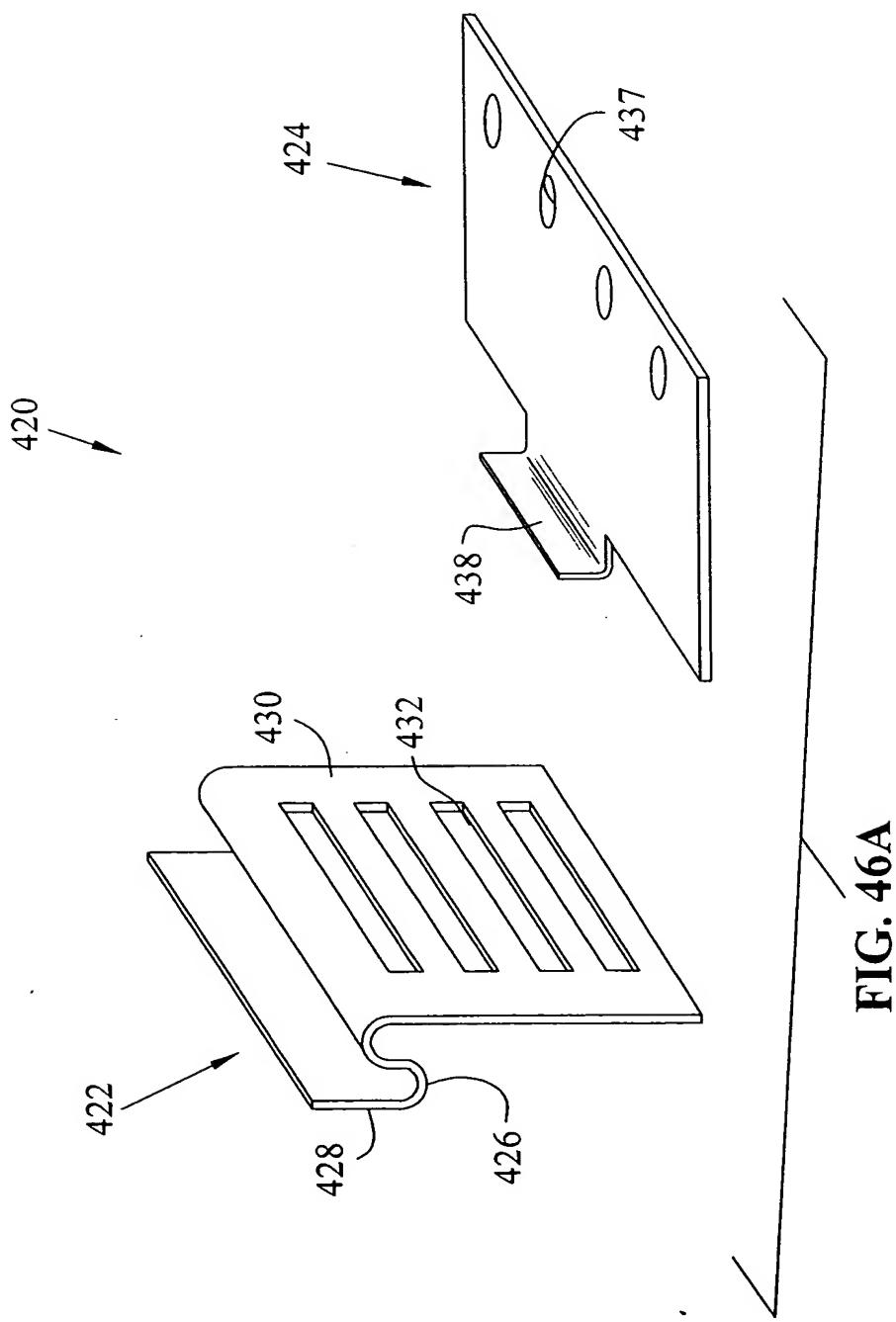


FIG. 46A

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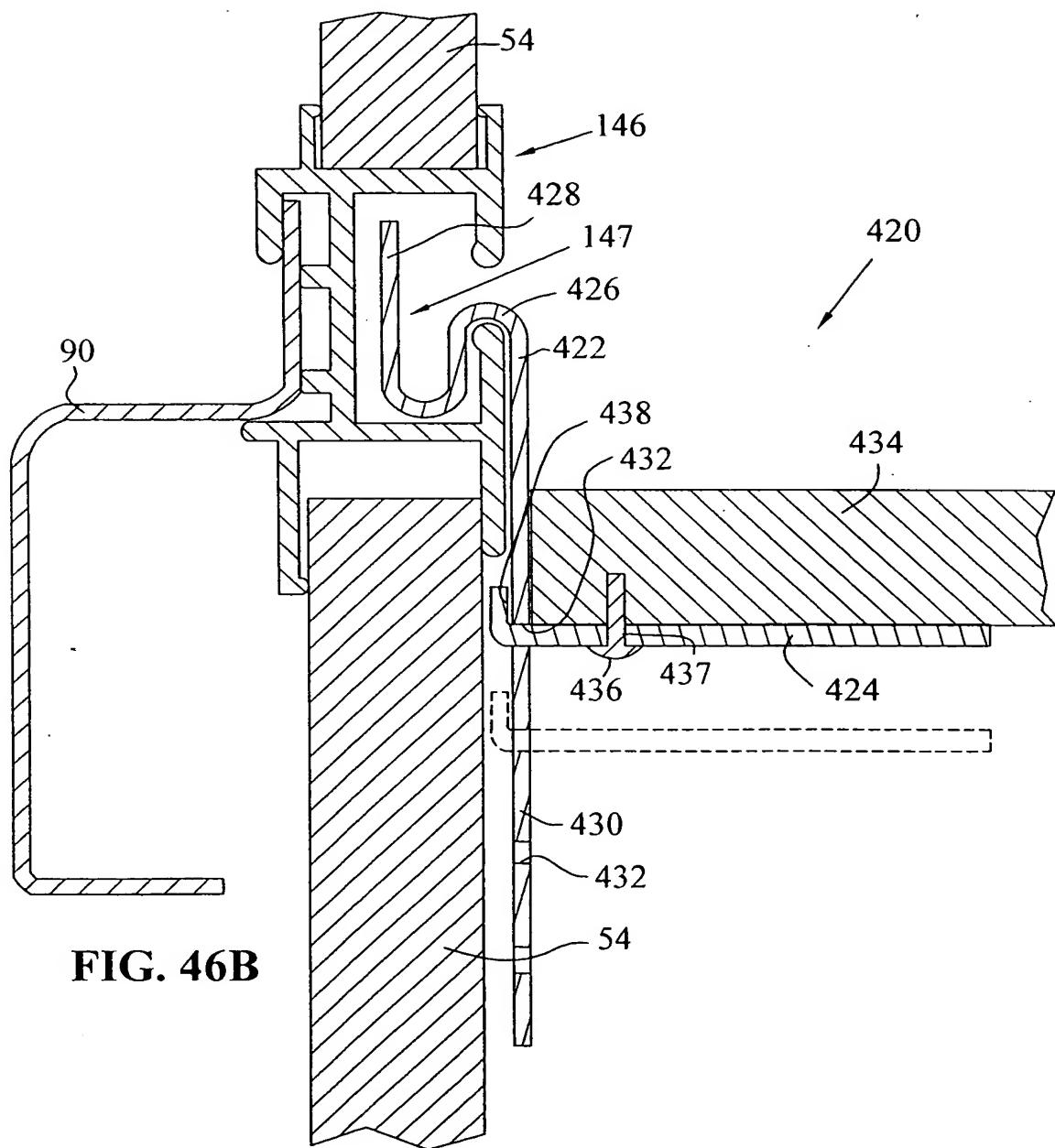
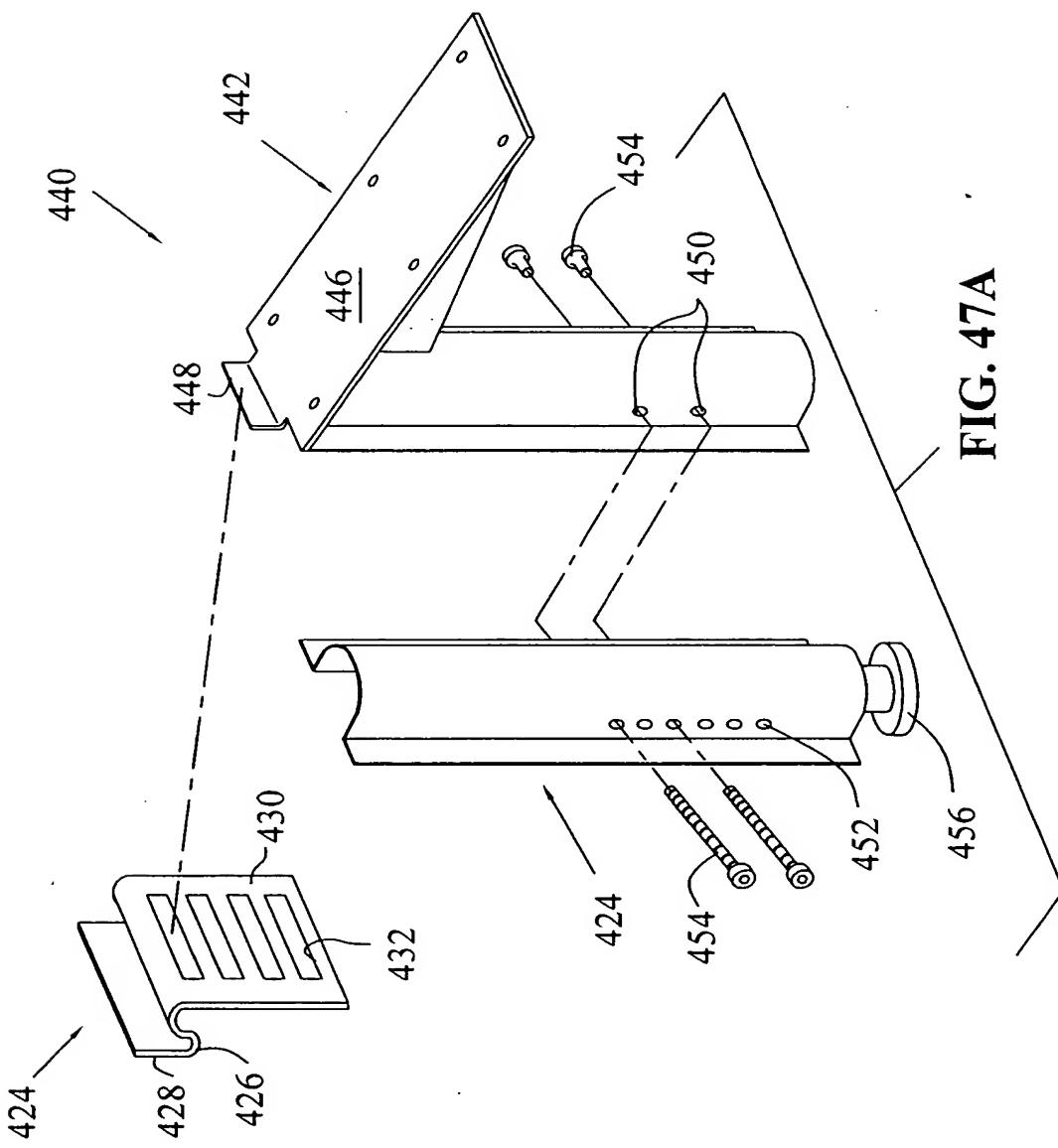


FIG. 46B

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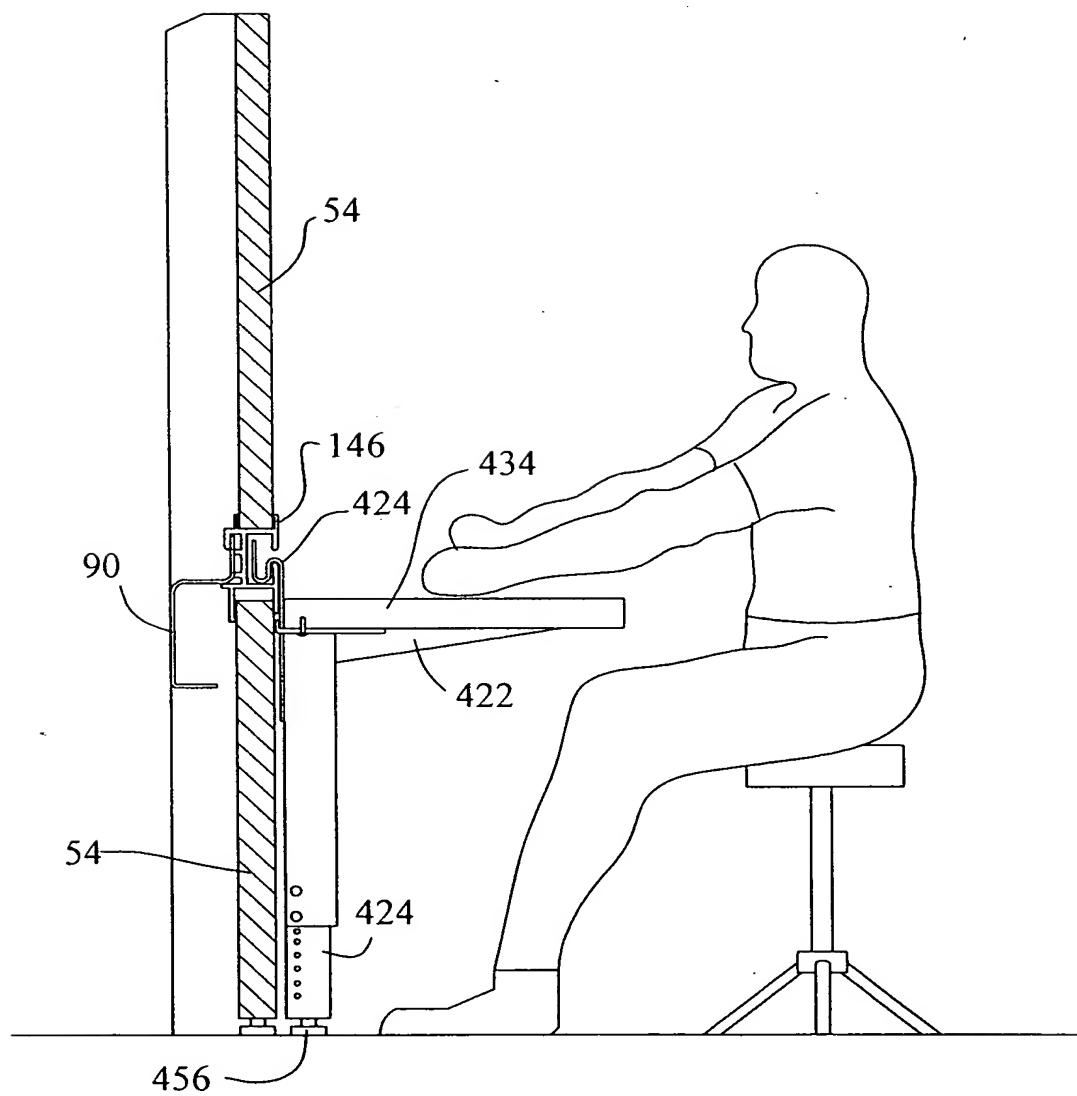


FIG. 47B